Abstract

A complementary differential amplifier which has RAIL TO RAIL feature with offset cancellation technique to enhance high color depth and high-resolution liquid crystal display (LCD) drivers, is proposed. The dual complementary differential pairs are used to obtain RAIL TO RAIL input-output swing and an offset canceling capability. Both offset voltage and injection-induced error, due to mismatch of device and charge injection, are greatly reduced. The offset cancellation and charge conservation, which is used to reduce the dynamic power consumption, are operated during the same time slot so that the driving period does not need to increase. It is implemented using LTSPICE simulation.

References

- A New Compact Low-Power High-Speed Rail-to-Rail Class-B Buffer for LCD Applications Davide Marano, Gaetano Palumbo, Fellow, IEEE, and Salvatore Pennisi, Senior Member, IEEE JOURNAL OF DISPLAY TECHNOLOGY, VOL. 6, NO. 5, MAY 2010
- T. Itakura and H. Minamizaki, "10-µAqiescent current opamp design for LCD
Charge Conservation Technique to Reduce Dynamic Power of Class AB Amplifier for LCD using LTSpice


Index Terms

Computer Science
Circuits And Systems

Keywords

CMOS  Class AB amplifiers  Rail-to-Rail  Push-pull stage  Complementary differential pair transconductance amplifiers.