Abstract

In this paper low power consumption amplifier is presented. Low power consumption amplifier is versatile demanded in modern technology, modern technology want such type of amplifier which gives low power consumption with less reflection, in this paper presented series and shunt feedback topology with pole zero compensation method for designing of low power consumption amplifier, Today the demand of integrated circuits are increasing day by day with increase in the number of elements in it. However, power and reflection should be less. The scaling of the components increase, increase its number, for the similar area of an chip reduces its sub-threshold voltage with its increase in the leakage power consumption.. The Power consumption is based on the number of elements and routing of components and its process fabrication. In this paper developed method to reduce leakage power consumption with reduction of reflection. Here design low power dissipation amplifier using compensation and feedback technique

References


**Index Terms**

Computer Science

Circuits and Systems

**Keywords**
Leakage power dissipation; feedback topology; Transistor; pole-zero Compensation technique