Abstract

Reversible logic is highly useful in nanotechnology, low power design and quantum computing. The paper proposes a power efficient design of an ALU, using Reversible Logic Gates. With power management becoming a critical component for hardware design developers, Reversible Logic can provide a viable alternative towards creating low power digital circuits.

References

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Power Efficient Arithmetic Logic Unit Design using Reversible Logic

17. NOVEL DESIGN OF OPTIMIZED MULTIPLEXER CIRCUIT USING REVERSIBLE LOGIC Vandana Shukla1, O. P. Singh1, G. R. Mishra1, R. K. Tiwari2 vandanashuklaco5@gmail.com, op singh@amity.edu, gr_mishra@rediffmail.com, rktiwari2323@yahoo.co.in, Amity School Of Engineering & Technology, Amity University, Lucknow 2Dr. R. M. L. University, Faizabad
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