All reversible circuits have an intrinsic advantage over traditional irreversible circuits, because they reduce power consumption. Due to this, reversible circuits have been a source of constant excitement and great enthusiasm in the scientific community. Reversible logic is highly useful in nanotechnology, low power design and quantum computing. This paper proposes a design for a faster adder using reversible gates.
Electronics Research Laboratory, “Dynamic Voltage Scaling and the Design of a Low-Power Microprocessor System”


12. Asher Peres, “Reversible logic and quantum computers”, The American Physical Society
Implementation of a Fast and Power Efficient Carry Select Adder using Reversible Gates


Index Terms

Computer Science  Circuits and Systems

Keywords

Reversible Logic, Efficient Adder designs, Carry Select Adder, Power Efficient