Abstract

In this Technical era the high speed and low area of VLSI chip are very-very essential factors. Day by day number of transistors and other active and passive elements are drastically growing on VLSI chip. All the processors of the devices adders and multipliers are played an important role. Adder is a striking element for the designing of fast multiplier. Ultimately here need a fast adder for high bit addition. In this paper, proposed Kogge-Stone adders are used for binary addition to reduce the size and increase the efficiency or processors speed. Proposing Kogge Stone adder provides less components, less path delay and better speed compare to other existing Kogge Stone adder and other adders. Here we are comparing the Kogge Stone adders of different-different word size from other adders. The design and experiment can be done by the aid of Xilinx 14.2i Spartan 3E device family.

References


**Index Terms**

Computer Science Information Sciences

**Keywords**