Abstract

This paper presents design methodology and comparison of a 1-bit DAC in both 180 nm and 90 nm CMOS technologies. A 1-bit digital-to-analog converter (DAC) is designed and simulated in
180 nm and 90 nm CMOS technologies using Cadence Virtuoso tool. This DAC can be used in the feedback path of Delta-Sigma modulators. Delta-Sigma ADC is the most robust type of ADC which is highly immune to noise compare to that of ordinary analog-to-digital converters.

References


Index Terms

Computer Science

Power Systems
Keywords
Dac, Delta-sigma Modulator, 90 Nm, 180 Nm