Abstract

The unconstrained clock skew scheduling is practically limited due to the difficulties in implementing a wide spectrum of dedicated clock delays in a reliable manner. This results in a significant limitation of the optimization potential. As an alternative multi domain clock skew scheduling technique with dedicated clock buffer will be implemented. In this paper, an algorithm to determine the minimum number of clock domains to be used for multi domain clock
A Novel Approach for Multi-Domain Clock Skew Scheduling

skew scheduling is presented. The experimental results show the optimized clock period, dynamic power consumption implemented on digital logic part of telephone answering machine.

Reference

A Novel Approach for Multi-Domain Clock Skew Scheduling


Index Terms

Computer Science

Wireless

Key words

Clock skew domain
clock skew scheduling (CSS) Low Power VLSI

Synopsys Design Compiler