Abstract

This paper hearsay on various design approach of transimpedance amplifier (TIA) that improves the performance along various parameters such as gain, noise, speed and bandwidth. Transimpedance amplifier design overcomes the drawbacks of high impedance amplifier design. Gain boost up can be done by using PMOS current source at the input stage. Capacitive coupling and cross coupled current conveyor stage trim down input noise and get better the speed of transimpedance amplifier.

References

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Meng Lim "Cross coupled current Conveyor based CMOS transimpedance amplifier for broad band data transmission"; IEEE transaction on very large scale integration (VLSI) systems, vol 21, No. 8, August 2013.


**Index Terms**

Computer Science  
Circuits And Systems

**Keywords**  
Transimpedance amplifier noise gain bandwidth enhancement cross coupled current conveyor

voltage headroom.