This paper presents the analysis and implementation of encapsulation schemes for baseband frame of DVB-S2 satellite modulator. As convergence is the main issue in broadcast communications, encapsulation schemes enable the carriage of network layer packets over DVB networks in an effective manner. In order to meet the requirements of different complex stages of the DVB-S2 baseband signal flow, the presented encapsulation schemes are efficient. Efficiency of encapsulation schemes under different criteria of DVB-S2 baseband frame is calculated. The baseband frame is first simulated & implemented on Xilinx ISE software tool for hardware realization. The framing model is tested on Zynq based Xilinx Field programmable Gate array (FPGA) Development Platform.
Gathering and other broadband satellite applications.
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Index Terms

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Keywords
MPE ULE Transport stream GSE Baseband frames