Abstract

This senate proposes an optimize version of the Hamming codes for Error detection and correction (EDAC) used in the HDLC protocol as HDLC being the most enduring and fundamental standard in communication. When data is either stored in memory or transmitted through a communication channel it is not errorless. With the exposure to electromagnetic radiation the semiconductor memory which are used in various applications get damaged. Because of electromagnetic interference the contents of the RAM memory cells get affected which leads to the bit flip in the magnetic storage devices like floppy disk, magnetic tape and hard disk devices etc. A proposed method has been developed to overcome the existing problems by using Xilinx ISE 13.2 simulator tool through which number of bit errors detection and correction can be increased in 8x8 matrix. It will result into enhancement of code rate and reduction of bit overhead.

References


11. Golay (23,12,7) code - Error Correcting Codes Page http://www.eccpage.com/golay23.c


Improving Performance Parameters of Error Detection and Correction in HDLC Protocol by using Hamming Method


Index Terms

Computer Science
Communications

Keywords

Error detection and correction (EDAC), High level data link control (HDLC) Hamming method.