Abstract

This paper presents the study and survey analysis of Full Subtractor circuit on implementing the MTCMOS technique. Full Subtractor is a combinational circuit that performs subtraction and results in difference and borrows outputs. Implementing the MTCMOS technique on this circuit results in reduction of both leakage current and power consumption.

References

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Index Terms

Computer Science Circuits and Systems

Keywords

Full Subtractor, MTCMOS, leakage current, power dissipation.