Sensorless Vector Controlled Multilevel Inverter Fed BLDC Motor

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ABSTRACT

BLDC motors are mostly known to be driven by trapezoidal control due to its simple implementation, but this type of control results in pulsating torque ripple which is unwanted in high performance drives. In this paper, vector control is combined with a five-level inverter to minimize the torque ripple of BLDC motor in sensorless operation as well as reducing the total harmonic distortion in the voltage and current waveforms. The MATLAB/Simulink environment is used to simulate and verify the proposed method.

Keywords

BLDC motor, multilevel inverter, sensorless control, torque ripple reduction, vector control.

1. INTRODUCTION

Brushless DC (BLDC) motors are becoming very popular in both commercial and industrial applications due to its many merits such as its high efficiency, high power density, low maintenance and lower electromagnetic interference (EMI).

These motors have the same structure as the brushed motors; they consist mainly of stator with windings and a rotor that contains permanent magnets instead of windings. The rotor is rotated by attraction between the electromagnets formed in the stator and the permanent magnets.

The conventional control for BLDC motors is six-step or trapezoidal control; this control energizes two phases at a time and leave the third phase floating; then, a rotating flux vector is formed in the stator that drags the rotor with it with appropriate switching signals relative to the rotor position. This control is very simple and is easy to implement; however, high pulsating torque ripple is associated with this type of control A way to improve the performance of BLDC drives is to drive the motor with vector control. This control is mostly used with PMSM drives and it can also be used with BLDC motors and this can minimize the torque ripple and improve the dynamic response of the drive.

Shucheng [1] proposed using Sinusoidal PWM with BLDC motor to reduce the torque ripple. The results shows reduction of about 50% of the torque ripple compared with the six-step control but the dynamic performance of the drive was not very efficient because of the absence of the current control loop and there is considerable harmonic components in current waveform.

Rau [2],Sensorless vector control is applied to a BLDC motor to achieve better efficiency and better dynamic response of the drive, the results showed improvement performance over sixstep control and a fast dynamic response. However, the torque and speed had a large ripple that should be minimized. Turki K. Hassan, PhD Supervisor Department of Electrical Engineering Almustansiriya University

Multilevel inverters are being used widely in medium and high voltage motors due to its many advantages over the twolevel inverter such as the reduction in the harmonic distortion, lower dv/dt which reduces the stress over switching devices, lower distortion of input current and the ability of operation at high and low switching frequencies.

Previous work has been done regarding the use of multilevel inverter with BLDC motors in [3-5] and results showed a lot of improvement over two-level inverter to lower the total harmonic distortion of the output voltage waveform; however, current distortion and torque transient response wasn't addressed by the authors.

The sensorless control of BLDC motor has been around for a while and it gives several advantages over the use of sensors that includes lower cost especially with the use of vector control where high resolution is required and the sensors become expensive, less space, improved reliability and the ability to work under high pressure and high temperature environments.

Many methods were proposed for position and speed estimation for BLDC motors; most of them are based on the detection of the back electromotive force (emf) of the floating phase [6-8]. These methods are simple and doesn't require complex computations; however, they are less efficient at low speeds because the signal of the back emf becomes low and distorted.

In this paper, a five-level inverter is used with vector control to achieve reduced torque ripple and good speed response for sensorless operation of BLDC motors. The method is tested and verified using MATLAB/Simulink environment.

2. BLDC MOTOR MODELING

BLDC motor works with the same principle as a synchronous motor; when the stator windings are energized with alternating three phase currents, a rotating magnetic motive force (mmf) is established; with a proper switching of the stator currents, this mmf drags the rotor by the force of attraction and the rotor rotates with the same frequency as the rotating field. The modeling of the motor is set by the following equations [9]:

$$v_a = e_a + i_a R_s + L_s(di_a/dt) \tag{1}$$

$$v_b = e_b + i_b R_s + L_s(di_b/dt) \tag{2}$$

$$v_c = e_c + i_c R_s + L_s(di_c/dt) \tag{3}$$

Where v_a , v_b and v_c are the Phase voltages in volts, e_a , e_b and e_c are the back emf of each phase in volts, i_a , i_b and i_c are the phase currents in ampere, R_s is the stator resistance in ohm and L_s is the stator self-inductance in hennery.

$$e_a = K_e f(\phi) . \omega(t) \tag{4}$$

$$e_b = K_e f(\phi - 2\pi/3) \omega(t) \tag{5}$$

$$e_c = K_e \cdot f(\phi + 2\pi/3) \cdot \omega(t) \tag{6}$$

Where ϕ is the rotor position in radians, **K**_e is the back emf constant in V/rad/sec, ω is the rotor speed in rad/sec and **f**(ϕ) is a function changes with the rotor position.

The developed electromagnetic torque is given by the equations:

$$T_{e} = (e_{a}i_{a} + e_{b}i_{b} + e_{c}i_{c})/\omega$$
(7)

$$T_e = K_t I_q \tag{8}$$

Where T_e is the electromagnetic torque in N.m, K_t is the torque constant in N.m/A and Iq is the quadrature current component in Amperes. The dynamic equation of the motor is giving by:

$$T_{e} - T_{L} = B.\omega + J(d\omega/dt)$$
(9)

Where T_L is the applied load torque in N.m, B is the friction coefficient in N.m.s and J is the moment of inertia of the motor in kg.m².

In order to achieve maximum torque, the angle between the stator magnetic field and the rotor magnetic field should be 90 degrees according to the equation:

$$T_{e} = F_{s} \times F_{r} \tag{10}$$

Where F_{s} is the force of the stator magnetic field and $F_{\rm r}$ is the force of the rotor.



Figure 1 BLDC Motor with Vector Control applied

With vector control applied to the motor, the angle between the two fields remains fixed at 90 degrees thus the dynamic response is greatly improved.

3. MULTILEVEL INVERTER

In order to get lower harmonic distortion in the output waveform, multilevel inverters can be used to synthesize the voltage into a number of levels. The higher is the number of levels, the closer is the output voltage waveform to a sinusoidal shape and therefore reducing the harmonic distortion [10]. There are a number of multilevel inverter topologies, the most popular are the cascaded H-bridge (CHB), neutral pointclamped (NPC) and the flying capacitor.

The NPC and flying capacitor topologies have a problem of voltage unbalance which can be more severe with over three levels. The CHB topology doesn't have this problem but have the disadvantage of the need of isolated DC sources.

The CHB method consists of a series connected H-bridges, each bridge has one DC source. The number of H-bridges depends on the number of levels generated by the multilevel inverter according to the following equation [11]:

$$M = (N - 1)/2$$
(11)

Where M is the number of H-bridges and N is the number of levels.

Sinusoidal pulse width modulation (SPWM) is one of the most popular modulation techniques in multilevel inverter modulation. It is based on a comparison between a modulating sine wave signal and a number of triangular carrier signals given by N-1.

There are different techniques for SPWM, mainly the level shifted modulation and phase shifted modulation [12]. In this paper, the level shifted modulation with phase disposition is used.

4. SPEED AND POSTION ESTIMATION

The speed and position are estimated using the rotor flux vectors which are found from the motor's equations as follows [12]:

$$\psi_{\alpha s} = \int (v_{\alpha} - i_{\alpha} R_s - \omega_o \psi_{\alpha s}) dt$$
(12)

$$\psi_{\beta s} = \int (v_{\beta} - i_{\beta} R_{s} - \omega_{o} \psi_{\beta s}) dt$$
⁽¹³⁾

Where $v_{\alpha}, v_{\beta}, i_{\alpha}$ and i_{β} are the voltages and currents in the α and β coordinates found by Clarke's transformation, $\psi_{\alpha s}$ and $\psi_{\beta s}$ are the stator fluxes in wb and ω_o is the cutoff frequency of the lowpass filter that is used to eliminate the dc offset caused by the integration. The cutoff frequency is set experimentally to 20 rad/s. The rotor flux vectors are then found by:

$$\psi_{\rm PM\alpha} = \psi_{\alpha s} - L_s i_\alpha \tag{14}$$

$$\psi_{\mathsf{PM}\beta} = \psi_{\beta s} - \mathbf{L}_s \mathbf{i}_\beta \tag{15}$$

Where $\psi_{PM\alpha}$ and $\psi_{PM\beta}$ are the rotor fluxes in wb. Then, the speed and position are extracted from a phase locked loop (PLL) structure as follows:



Figure 2 PLL structure

Where ω_e and Φ_e are the electrical speed and electrical position of the rotor respectively. PLL is a method used to detect the phase and frequency of a signal. The above structure is a quadrature-PLL (Q-PLL) used when the inputs are two orthogonal signals [13].

5. PROPOSED METHOD

The proposed scheme is shown in figure 3. The BLDC motor is fed from a five-level cascaded H-bridge inverter; the inverter gate signals are provided by level- shifted phase disposition SPWM. The I_d and I_q currents are obtained from transforming the three phase currents by Clarke and Park transformations and the two currents are controlled by proportional-integral (PI) controllers. The speed and position are obtained from the speed estimator block; the estimated speed is first filtered by a low pass filter (LPF) and is then compared with the reference speed. The estimated position is used in the Park and Park Inverse transformations. The output of the currents controllers are transformed from d-q coordinates to a-b-c coordinates by Clarke inverse and Park inverse transformations; these signals represent the reference per unit voltages which are given to the SPWM block to be compared with the carrier signals to generate the gate signals for the multilevel inverter.



Figure.3 Block diagram of the proposed method

6. SIMULATION RESULTS AND DISCUSSION

The simulation was built using MATLAB/Simulink program. The motor specifications are listed below:

Rated Voltage	500 v
Rated Speed	3000 rpm
Stator Resistance	2.875 Ω
Stator Inductance	8.5 mH
Number of Poles	8
Moment of Inertia	0.8 *10-3 Kg.m2
Friction Coefficient	1 mNm.s
Rated Torque	3 Nm
Voltage Constant	0.146 v/rpm
Torque Constant	1.4 Nm/A

Table 1. Motor specifications

The following figures show the obtained simulation results:



Figure.4 Actual and estimated rotor speed

Since the rotor initial position is not known, the motor is first driven in open-loop mode by v/f ramp until the motor picks up the speed and the control is switched to sensorless mode. At time 1 second, a 3 N.m load is applied to the motor; the speed drops of about 70 rpm and then returns after 0.7 sec to the reference speed.



Figure.5 Current, back emf and electromagnetic torque of trapezoidal control at rated speed





Figure.6 Current, back emf and electromagnetic torque for the proposed method



Figure.7 Five-level phase voltage and nine-level line-line voltage at rated speed

From the figures 5-7 it is seen that the current of the proposed method is close to a sinusoidal shape with a total harmonics distortion (THD) of less than 5% which is a reduction of more than 600% of the current THD in the trapezoidal control. The back emf is shown to be trapezoidal in shape in both controls although the current shape is different and that is because its shape mainly depends on how the windings are distributed in the stator. The torque as shown in the proposed method have a ripple percentage of about 20% at rated speed which is a reduction of about 70% compared with the traditional trapezoidal control.



Figure.8 Dynamic performance of the motor

Figure.8 shows the dynamic performance of the motor when the speed is changed from 3000 rpm to 1500 rpm and then to 500 rpm. The speed response is robust and is shown to track the reference speed efficiently even at low speeds with a little overshoot of 0.02 and steady state error of about 0.26% and speed ripple of 0.5 rpm at high and medium speeds and about 4 rpm at low speeds.



Figure.9 Current, back emf and electromagnetic torque of trapezoidal control at 500 rpm



Figure.10 Current, back emf and electromagnetic torque of the proposed method at 500 rpm

Figures 9,10 show the torque ripple at 500 rpm is about 30% which is still a reduction of approximately 55% as compared with the traditional method.

The total harmonic distortion (THD) for the current and lineline voltage for the trapezoidal and proposed method are shown in the following figures:



Figure.11 Line-line voltage spectrum and THD for trapezoidal control



Figure.12 Current spectrum and THD for trapezoidal control



Figure 13 Line-line voltage spectrum and THD for the proposed method



Figure 14 Current spectrum and THD for the proposed method

Figures 11-14 show the obtained THD of the current and voltage waveforms in the trapezoidal control and the proposed method. As is observed, the THD for the current and voltage waveforms in the proposed method are much less than that of the trapezoidal control.

7. CONCLUSION AND FUTURE WORK

In this paper, cascaded H-bridge five-level inverter with vector control are proposed to be used with BLDC motors in sensorless operation using a PLL structure.

The proposed method is compared with the traditional trapezoidal control through a number of simulations.

The simulation results obtained showed a great reduction of torque ripple as compared with the traditional trapezoidal control due to the reduction of the distortion in the voltage and current waveforms. The results also showed a good speed performance in sensorless operation for both steady and transient states.

A possible future work is to implement this method experimentally using field programmable gate array (FPGA) or microcontroller.

8. REFERENCES

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