Abstract

In the current scenario, energy optimization for the electrical components used in the hand-held devices is a broad area of research. Energy principle for information technology contains its own specific energy behavior. The energy costs in server centre are now compatibility to the cost of hardware devices and other compare devices. In the processor device system, heat existing is a major cause of limiting changes in the performance evaluation. In laptop, scanners, computers, cell phones, printers, i-pods, and other digital devices, which are portable, reduced the power consumption converts into the battery long life manner. The energy consumption is now presenting challenges as a performance measure in computers, processing the task execution time. The energy is linked with the execution time capacity, where comparable patterns have been recognized by a combination of hardware devices, software devices, and algorithms. In this paper, the design of energy-efficient computer systems is proposed by the use of Bellman-ford algorithmic approach. A model is proposed for finding the performance of the system. Computed results are depicted in the form of tables and graphs.
References


**Index Terms**

Computer Science  
Algorithms
Keywords