

Design and Analysis of CMOS Microstrip Antenna using Matlab

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ABSTRACT

The concept of microstrip antenna^[1] was first proposed by Deschamps in 1953. The microstrip antennas differ from others in a way that they radiate from a microstrip patch or a modified TEM line or slot in the ground plane. The active microstrip antenna^[6] has become prominent now a day in the field of microwave communication because of their numerous advantages such as light weight, low volume, easy fabrication, low profile and suitability for mass production.

Tremendous research and development in the field of microstrip antenna has taken place all over the world due to use in the guided weapons satellite communications and other strategic defense equipments owing to their inherent advantage over conventional microwave antennas, such as light weight, low volume and compatibility with microwave integrated circuits and solid state devices. However the major problem of microstrip antenna is their narrow bandwidth. The circular patches are probably the most extensively used which have very limited bandwidth so that it has very limited applications in the practical case. In this paper the microstrip antenna is designed which is loaded with MOS capacitance and analyze the input impedance, VSWR, reflection coefficient etc., first for unloaded circular patch microstrip antenna^[2] and then for the loaded antenna with MOS capacitor^[3].

General Terms

Antenna, Circular patch antenna, Radio Antenna

Keywords

Microstrip

1. INTRODUCTION

An antenna is a system of elevated conductors which couples or matches the transmitter or receiver to free space. A transmitting antenna connected to transmit by a transmission line, forces EM waves into free space which travel in space with velocity of light. Similarly a receiving antenna connected to a radio receiver, receives a portion of EM waves travelling through space. Thus the radio antenna is defined as structure associated with the region of transition between a guided wave. According to IEEE antenna is simply defined as radiating or receiving radio waves.

In its simplest configuration a microstrip antenna consists of radiating patch on one side of a dielectric substrate (≤ 10), which has a ground plane on the other side. Microstrip antenna consists of very thin $h \leq \lambda_0$ where λ_0 is the free space wavelength metallic strip (patch) placed a small

fraction of wavelength ($h \leq \lambda_0$, usually $0.003\lambda_0 \leq h \leq 0.05\lambda_0$) above the ground.

There are numerous substrates that can be used to design a microstrip antenna and their dielectric constants are usually in the range of $2.2 \leq \epsilon_r \leq 12$. The ones that are most desirable for antenna performance are thick substrate where as dielectric constants in lower end of the range because they provide better efficiency larger bandwidth, loosely bound fields for radiation in the space. Ideally, the dielectric constant ϵ_r should be low (~ 2.5), so as to enhance fringe fields which account for the radiation. The patch conductors, normally of copper and gold, can assume virtually any shape, but conventional shapes are generally used to simplify analysis and performance prediction. Often microstrip antennas are also referred to as patch antennas. The radiating elements and the feed lines are usually photo etched on the dielectric substrate. The radiating patch may be square, circular, rectangular[5], elliptical etc.

2. MICROSTRIP ANTENNA CONFIGURATIONS

The two most serious limitations of the microstrip antennas are narrow bandwidth and low gain. The requirement for a low volume and low profile in the antenna further deteriorates these two parameters. This is because of the fact that there is a fundamental relationship between the size, bandwidth and efficiency of an antenna. As antennas are made smaller, either the operating bandwidth or the antenna efficiency must decrease. The gain is also related to the size of the antenna, which is small antenna typically provides lower gain than larger antenna. till date, with the key design considerations such as the size reduction, together with the bandwidth and gain enhancement in wireless communication[4], many researchers have developed various techniques to enhance the bandwidth and gain of the microstrip antenna and some of the techniques are loading of high permittivity dielectric substrate, stacked configuration and slotted patch antenna[8] The use of the substrate loading technique helps to increase the radiation efficiency of microstrip antenna.

3. DESIGN SPECIFICATION OF CIRCULAR PATCH MSA

In The analysis of circular patch MSA is done with the following details.

3.1 Unloaded circular patch MSA

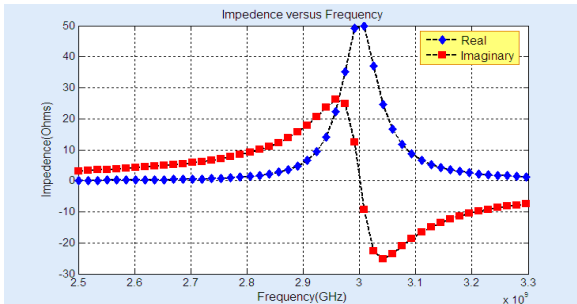
Relative dielectric constant = 2.32

Thickness of dielectric substrate $h=0.00159\text{m}$

Radius of circular patch $a=0.01830299\text{m}$
 Permittivity of vacuum $\epsilon_0=8.85 \times 10^{-12}$
 Feed Point of circular patch $r_0=a/4.398$
 Conductivity of the patch $\sigma=5.8 \times 10^7$
 Electric field $E_0=1\text{V/m}$
 Relative Permeability of $\mu=4\pi \times 10^{-7}$ the substrate
 Loss tangent $\delta=0.0001$
 Design frequency of the patch $f=3\text{GHz}$

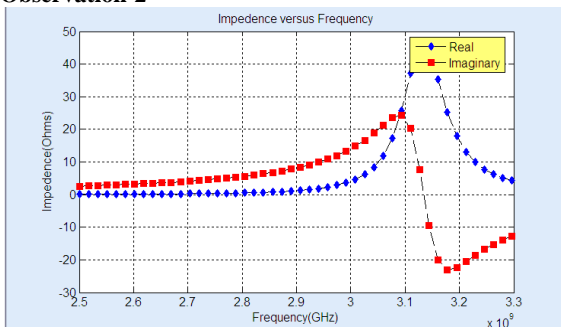
4. RESULTS

Observation-1



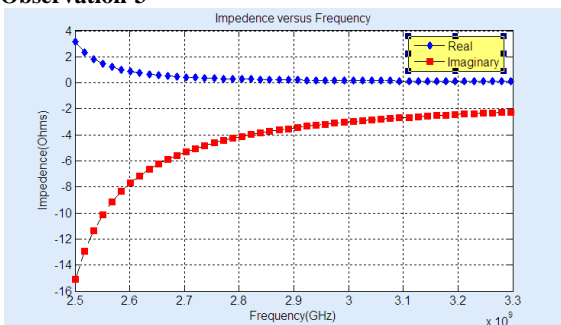
Variation of impedance vs frequency at $\epsilon_r=2.32$

Observation-2



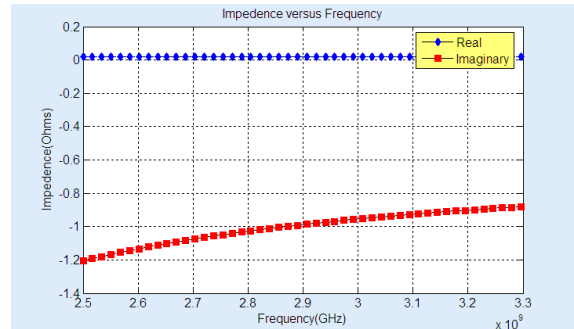
Variation of impedance vs frequency at $\epsilon_r=2.1$

Observation-3



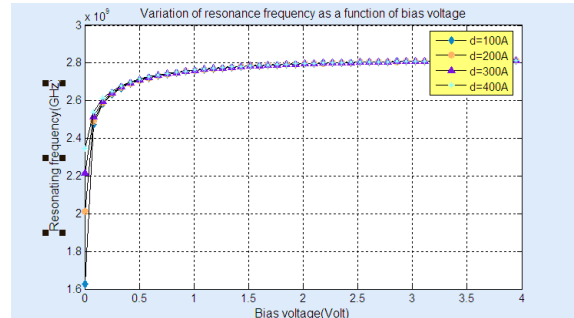
Variation of impedance vs frequency at $\epsilon_r=3.78$

Observation-4



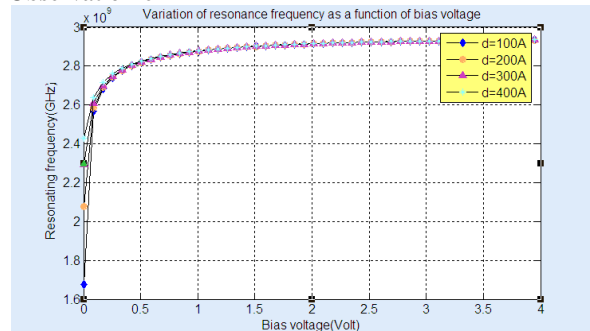
Variation of impedance vs frequency at $\epsilon_r=9.4$

Observation-5



a). Keeping relative dielectric constant = 2.32

Observation-6



b). Keeping relative dielectric constant = 2.1

5. CONCLUSION & FUTURE SCOPE

In The initial stage, An antenna can be standard out by calculating some antenna parameters such as the input impedance, VSWR, reflection coefficient etc., first for unloaded circular patch microstrip antenna and then for the loaded antenna with MOS capacitor. The variation of these antennas parameters with respect to frequency has been graphically obtained and observed, which are shown in the figure.

The operating frequency of the unloaded antenna is 3.0085GHz at which the peak of the real part of the impedance is shown. At this the impedance is shown. At this the impedance is 49.97Ω but it can be seen that as the frequency is increasing the impedance first increase s to its peak value then decreases to almost zero value. After loading, the input impedance decreases with frequency but peak remains the same at the operating frequency. The unloaded antenna has the only frequency at which it can work properly whereas the MOS capacitor loaded antenna has a range of frequencies to work without any malfunction, this feature is called tenability. The frequency

may be varied by varying the reverse bias voltage of MOS capacitor.

Quality factor should be kept high to increase the selectivity of loaded antenna. As shown in the graph plotted, the quality factor increases up to resonance frequency [7] and then starts decreasing whereas the Quality Factor is quite low as in case of unloaded antenna.

The resonance frequencies are plotted as the function of bias voltage for various oxide thicknesses (1000Å to 4000Å) for TM₁₀ mode. It is observed that the variation of the MOS capacitor is similar for all four oxide layers thickness. Larger variation in capacitance is observed when with bias voltage near zero bias, but it is very small at higher bias. The variation in resonance frequency in bias voltage for modified TM₁₀ mode is inversely proportional to the oxide thickness. The maximum frequency agility is obtained for minimum oxide thickness.

6. REFERENCES

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