Abstract

In this paper a 2D-DWT architecture can be implemented using 5/3 lifting scheme. The 2D-DWT is used in many fields, they are signal analysis, computer vision, object recognition, image compression and video compression. The DWT is developed by the JPEG2000 which is the one of image compression standard based on the wavelet coding techniques.

The proposed architecture is RISC based architecture. The RISC processor has simple instruction set and gives high performance. The 5/3 lifting scheme is used to implement these architecture, which performs predict and update steps. The main objective of the project is to implement RISC based 2D-DWT architecture for processing images with high compression ratio's[i.e. limited size and better quality] compared to DCT which is one of the transform used in image compression.

References
1. Nageswara Rao Thota1, Srinivasa Kumar Devireddy2 “Image Compression Using Discrete Cosine Transform”


**Index Terms**

Computer Science  
Image Processing

**Keywords**

DWT, DCT, JPEG2000, RISC processor, 5/3 lifting.