Abstract

The pre-encoded multipliers for encoding is quite useful for the digital signal processing in various applications of communication and data processing devices. The modified booth encoder proposed in this work is the technique to simplify the products implementation with the improvements in power consumption. The proposed architecture has less power than previous architecture. The synthesis results show the power required for proposed architecture is 14mW only. This will work longer on the same power as given to previous design.

References


**Index Terms**

Computer Science  
Circuits and Systems

**Keywords**

Pre-encoded, Booth Multiplier, Encoder