A Novel Power Efficient Pre Encoded Modified Booth Multiplier Encoder

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Abstract

The pre-encoded multipliers for encoding is quite useful for the digital signal processing in various applications of communication and data processing devices. The modified booth encoder proposed in this work is the technique to simplify the products implementation with the improvements in power consumption. The proposed architecture has less power than previous architecture. The synthesis results show the power required for proposed architecture is 14mW only. This will work longer on the same power as given to previous design.

References

3. B. C. Paul, S. Fujita and M. Okajima, "ROM-Based Logic (RBL) Design: A Low-Power 16


Index Terms

Computer Science

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Keywords

Pre-encoded, Booth Multiplier, Encoder