Implementation of Smart Health Care system using Zig-Bee enabled RFID and FPGA Technology

Pranali Patil  
PG Student (VLSI Design and Embedded Systems)  
KLE Dr. M. S. Sheshgiri College of Engineering and Technology, Belagavi, India

Udaykumar Naik, PhD  
Professor (Electronics and Communication)  
KLE Dr. M. S. Sheshgiri College of Engineering and Technology, Belagavi, India

Vijay Rayar  
Assistant Professor (Electronics and Communication)  
KLE Dr. M. S. Sheshgiri College of Engineering and Technology, Belagavi, India

ABSTRACT
In today’s healthcare domain Field Programmable Gate Array (FPGA) based platforms using Zig-bee based wireless network helps in medical diagnosis. This paper proposes a simplified real time FPGA system for human physiological purposes. Innovations like the output is observed at the transmitter side on LCD as well as at the receiver side on the display screen did successfully. In the period of developing population, basic issues in medicinal services framework made the nation some way or the another battling against the wasteful medicinal services. The system provides a simplified testing platform for blood pressure and virtual signal measurements. With the progression of electronic industry, the chip out of date quality is a noteworthy concern. The versatile VLSI configuration is a developing pattern to move from bespoke custom chip based framework to the delicate center processor inserted inside FPGA due to the requesting favourable circumstances like low NRE cost, low time to advertise, less equipment, amazing structure adaptability and reinvent capacity, low power utilization and rapid execution. In electronic industry there are such a significant number of zones, similar to remote interchanges, mechanical autonomy and so forth, where versatile structure has been actualized. In this angle, an endeavour has been made to plan what’s more, actualize a propelled human services framework in this article utilizing Zig-bee enabled RFID innovation as per the progressed VLSI plan of the processor. The framework is extremely quick and cost effective obtaining nearly 0% recognizable identification error. Xilinx ISE 14.3 simulator system has been utilized to simulate the processor module and so as to substantiate our structure we have utilized high performance Spartan 6 FPGA board.

Keywords  
RFID, Zig-Bee, Spartan 6 FPGA, Xilinx ISE 14.3, VLSI, Sensors.

1. INTRODUCTION
Recent times RFID technology has emerged as power tool in tracking and identification. RFID is effectively utilized in different applications like inventory network the executives to follow things, in war field, military area, in the administration of traffic, following of creatures in woodlands, route of robot etc[1,2,3]. Following gives better coordination and control in such cases. RFID innovation adds higher versatility to the generation [4]. RFID-introduced ID cards have researched an extraordinary window for Health care framework. RFID utilized in medicinal services framework incorporates social insurance worker or medical staff distinguishing proof card, persistent recognizable proof card, lower leg or wristband ID card and implantable RFID chips.

This project has a keen smart health care unit/ward endeavoured to structure and execute. The system utilizes the Zig-Bee empowered RFID technology. Zig-Bee depends on IEEE 802.15.4, that gives clients a financially efficient standard with capacity to keep running for a considerable length of time on cheap essential batteries for average applications. Zig-Bee gadgets work in unlicensed groups of ISM (Industrial, Scientific, Medical) 2.4 GHz band. The scope of Zig-Bee can be reached out from 10 meters to 75 meters and is subject to the power yield of the gadgets and the inclusion territory. Zig-Bee achieves its engaging low control usage. At the point when a centre point is connected with it passes on and returns to rest mode cutting down the power usage feasibly [4, 5]. Extra to modified power control inside a predefined domain/zone/ward, the proposed processor will record the patient ID present here, restorative report of each patient and update information, distinguish the ID of authority/support/pro present in this zone and record the arranging as well.

2. IMPLEMENTATION
In the transmitter section, Spartan 6 FPGA, Zig-bee module, BP module, RFID module and RFID chips are used. Initially, each patient is provided with a RFID tag, which has a unique ID. The BP is measured through a BP module and it is transmitted to the FPGA. Using RFID module, the RFID tag information is sent to the FPGA. Zig-bee is a transceiver which transmits the data to the FPGA. Finally the result is displayed on the monitor. The FPGA kit also consists of a temperature sensor, so that the temperature is also displayed. Now on the display screen, the patient’s id, BP module, RFID module and RFID chips are used. Initially, each patient is provided with a RFID tag, which has a unique ID. The BP is measured through a BP module and it is transmitted to the FPGA. Using RFID module, the RFID tag information is sent to the FPGA. Zig-bee is a transceiver which transmits the data to the FPGA. Finally the result is displayed on the monitor. The FPGA kit also consists of a temperature sensor, so that the temperature is also displayed. Now on the display screen, the patient’s id, BP and the temperature will be displayed. The block diagram of transmitter is shown in the figure 1.

- **RFID Module**: RFID module is related to EM-18 RFID module. By using this board with FPGA or microcontrollers to read the given card’s data is very simple and it requires simple serial connection. The board consists of power indicating LED which is red in colour and indicates the detection of card.
- **Zig-bee Module**: The Zig-bee module used here is CC2500 which is an ease 2.4GHz handset which is utilized for exceptionally low control remote applications.

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• **BP Module:** The BP measured of the patient is displayed on the FPGA as well as on the display screen of the monitor.

![Block diagram of Transmitter](image1)

In the receiver section, UART, Zig-bee module and display monitor is present. As Zig-bee is a transceiver, the data transmitted through the transmitter is received at the receiver by using UART as an interface. The received data will be displayed on the monitor. The block diagram of receiver is shown in the figure 2.

- **UART Module:** The Zig-bee and UART are interconnected. And finally the UART is connected to the display screen for the display of the output.

The final output displayed on the monitor consists of

i. The patient id.

ii. The blood pressure of the patient.

iii. The room temperature of the ward.

![Block diagram of Receiver](image2)

3. **SYSTEM IMPLEMENTATION AND RESULTS**

![ADC simulation waveform](image3)

The above figure represents the waveform of ADC simulation. When the CS is low and CLK is high the following results are obtained.

![Output of transmitter section of Patient](image4)

![Output at the receiver section of Patient](image5)
The above figure is the output received at the receiver section. The first digit indicates the Patient id, the next three digits indicate the blood pressure of the respective patient and the last two digits indicate the room temperature. Here the patient id is 1; the blood pressure of the respective patient is 087 and the room temperature is 25°C.

4. SYNTHESIS REPORT
Simulation result shows desired performance output to clarify the operations of the developed processor. The processor module has been synthesized, implemented successfully.

Advanced HDL Synthesis Report
- # Adders/ Subtractors: 26
- # Counters: 9
- # Registers: 265
- # Comparators: 29
- # Multiplexers: 232

Device utilization summary: Selected Device: 6slx9tqg144-3
Slice Logic Utilization:
- Number of Slice Registers: 396 out of 11440 3%
- Number of Slice LUTs: 650 out of 5720 11%
- Number used as Logic: 650 out of 5720 11%

Slice Logic Distribution:
- Number of LUT Flip Flop pairs used: 713
- Number with an unused Flip Flop: 317 out of 713 44%
- Number with an unused LUT: 63 out of 713 8%
- Number of fully used LUT-FF pairs: 333 out of 713 46%
- Number of unique control sets: 24

IO Utilization:
- Number of IOs: 20
- Number of bonded IOBs: 20 out of 102 19%

Specific Feature Utilization:
- Number of BUFG/BUFGCTRLs: 4 out of 16 25%
Engineering and Technology, Belagavi, Karnataka, India for encouraging me for this work.

7. REFERENCES


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