# Modelling and Simulation of Automatic Voltage Regulator System

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# ABSTRACT

For obtaining efficient stability and good regulation of different devices in power system and industrial applications, automatic voltage regulators (AVR) are increasingly used. However, AVR without any controller will provides slow responses due to disturbance and may cause instability. In this study therefore, the objective is to consider a generator AVR system without PID controller and with PID controller, where the PID controller was tuned with a view for improving the response of the system and make comparison between the frequency deviation step response and the tuned PID controller block performance using linear block model and control techniques in MATLAB / Simulink environment, in which the design configuration and automated PID turning was used to tuned the PID controller for AVR system without and with PID controller for generator 1 and generator 2 respectively. Simulation results indicates that generator 1 shows higher frequency overshoot and oscillation as compared to generator 2, which shows low frequency overshoot and minimum oscillation. The performance response of the tuned AVR system in generator 2 with PID controller gives satisfactory settling time which was recorded at 1.44 second as against 5.54 second.

# **General Terms**

Voltage Stability

Keywords AVR. PID

# **1. INTRODUCTION**

The automatic voltage regulator (AVR) is widely used in electric power systems and industrial applications to obtain the stability and good regulation of different apparatus. It may be use as an electromechanical mechanism, passive or active electronic components. Depending on the design, it may be use to regulate AC or DC voltage [1]. Thus, maintaining constant voltage at the output terminal of a generator is essential for satisfactory mains power supply. The terminal voltage can be affected by disturbances, such, as change in load, temperature, speed e.t.c, such that voltage regulating equipment is required to keep the voltage constant and maintain continuous supply within acceptable quality. To achieve this, automatic voltage regulator (AVR) system is used in power generation site to ensure voltage stability at the generator terminal. However, AVR system without any controller will provide slow responses and may cause instability. Thus, need to provide proper control of generator by AVR system to ensure generators operated at safe and stable conditions for normal operations and fast response under different type of disturbance is very essential.

Nevertheless, AVR cannot respond to change in generator output voltage quickly due to the resultant oscillation when it subjected to disturbances, thus the AVR cannot effectively regulate steady state error. Thus, to improve the dynamic response as well as to reduce the steady state error in the AVR loop, a linear PID controller is used in this paper in order to study the responsiveness of the controller, the degree to which the controller overshoots the set point and the degree of system oscillation.

According to [2] AVR system usually consists of an amplifier, exciter, generator and a sensor. Exciter, which is the source of electrical power for field winding of a generator controlled by the AVR, is very effective during steady – state operation. But

The work by [3] observed that in case of sudden disturbances it may have negative influence on the damping of power swing. This may be reduced or eliminated by introduction of a supplementary linear controller, the PID controller which will be able to produce additional signal into the AVR control loop and in that way compensates voltage oscillations. In mid 1930s three mode controllers with proportional, integral and derivative (PID) actions were commercially available and gained widespread acceptance. And recently different linear control algorithms have been developed so far. However, the PID – type controller is still the most widely used control strategy in practice and 90% of all the industrial controllers are of the PID – type. This is as a result of its simplicity in design, adequate tuning capabilities and good performance [4].

Control of AVR system has been proposed in [5]. The authors designed an AVR system using MATLAB/Simulink and the performance of the system was evaluated using PID controller. It was deduced that the proportional controller (Kp) have the effect of reducing rise time, the integral controller (Ki) have the effect of eliminating steady - state error, and the derivative controller (Kd) have the effect of increasing stability of the system. In this work, the combined action of the three controllers will be tested as a single controller to determine their resultant effect instead of using them separately in the AVR system.

The work by [6] presented the development of excitation system in simulation environment. The excitation system and single machine modes are implemented using MATLAB/Simulink. The author provides some advices in parameter estimation of excitation system and highlights that transfer function model will be more useful in power system studies.

Literatures indicate that the merit of using PID controllers lies in its simplicity and good performance. In this study however, two cases will be considered, an ideal AVR system without PID controller and AVR system with PID controller for generator system 1 and 2 respectively. Where the PID controller will be tuned with a view for improving the response of the system and make comparison between the frequency deviation step responses of the AVR and the tuned PID controller's block performances. The objective is to consider a generator AVR system and find its steady state frequency deviation step and terminal voltage step response using linear control technique called PID controller using MATLAB/ simulink.

### 2. CONCEPT OF AUTOMATIC VOLTAGE REGULATOR (AVR)

AVR functions when there is voltage error occurrence between terminal voltage and reference voltage as a result of any disturbance in power system network. The regulation element is commanded up to a point to produce a higher output voltage by dropping less of the input voltage; if the output voltage is too high, the regulation element will normally be commanded to produce a lower voltage. A schematic of an AVR system is shown in figure 1. The voltage magnitude is sensed through the potential transformer on one phase. This voltage is rectified and compared to DC set point signal. The amplifier error signal controls the exciter field and increases the exciter terminal voltage. Thus, the generator field current is increased, which result in an increase in generated e.m.f. The reactive power generation is increased to a new equilibrium, raising the terminal voltage to desired value [7].

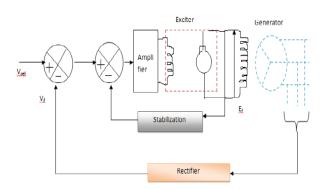


Figure 1: Systematic AVR System

Figure 2 describes the transfer function model of an AVR system. The generator terminal voltage  $V_t$  is compared with a voltage reference  $V_{ref}$  to obtain a voltage error signal  $\Delta V$ . This signal is applied to the voltage regulator shown as a block with transfer function  $K_A/(1 + TA_s)$ . The output of the regulator is then applied to exciter with a block of transfer function  $\frac{K_e}{(1+Te_s)}$ . The output of the exciter is then applied to

the field winding which adjusts the generator terminal voltage. The generator field can be represented by a block with a transfer function

 $K_F/(1+S_{Tf}).$ 

The total transfer function is given

$$\frac{\Delta V}{\Delta V r e_f} = \frac{G_{(S)}}{1 + G_{(S)}} \tag{1}$$

Where, 
$$G_{(S)} = \frac{K_{A K_{e K_{f}}}}{(1 + S_{TA})(1 + S_{Te})(1 + S_{Tf})}$$
 (2)

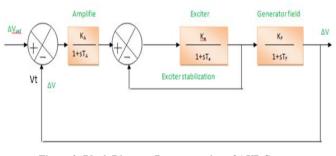


Figure 2: Block Diagram Representation of AVR System Source: (elaerning.Vtu.in)

#### 3. MODELLING OF THE AVR SYSTEM

Figure 3.1 shows the basic components of an AVR system. It consists of a comparator, amplifier, exciter and generator [8].

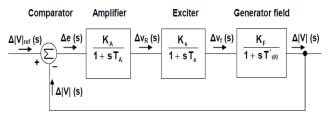


Figure 3.1: Block Diagram of an AVR System

It is to be noted that error voltage  $e = |V|_{ref-}|V|$  (3) Assume that for some reason the terminal voltage of the main generator decreases. This will result in decrease in |V|. this will immediately results in an increased "error voltage" ewhich in turn, causes increased value of  $V_{R}$ ,  $i_e, V_f$ , and  $i_f$ . With increase in  $i_f$ , the generator field flux also increases. Thus, raising the magnitude of the internal generator e.m.f and hence the terminal voltage.

For the comparator,  $\Delta |V|_{ref} - \Delta |V| = \Delta e$  (4)

Taking the Laplace transform of equation (2) gives;

$$\Delta |V|_{ref} (S) - \Delta |V|(S) = \Delta e(S)$$
<sup>(5)</sup>

For the amplifier,  $\Delta V_R = K_A \Delta e$ 

Where  $K_A$  is the amplifier gain

Laplace transform of equation (4) yields;

$$\Delta V_R(S) = K_A \Delta e(S) \tag{7}$$

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(6)

This equation implies instantaneous amplifier response. But in reality, the amplifier will have a time delay that can be represented by a time constant  $T_A$ .

Then,  $\Delta V_R(S)$  and  $\Delta e(S)$  are related as

$$\Delta V_R(S) = \frac{\kappa_A}{1 + sT_A} \Delta e(S) \tag{8}$$

Here,  $\frac{K_A}{1+sT_A}$  is the transfer function of the

amplifier  $G_A(S)$ .

For the exciter field, if  $R_e$  and  $L_e$  represent respectively the resistance and inductance of the exciter field, then

$$V_R = R_e i_e + L_e \frac{d}{dt} i_e \text{ and hence}$$
$$\Delta V_R = R_e \Delta i_e + L_e \frac{d}{dt} (\Delta i_e)$$
(9)

The exciter field current  $i_e$  produces voltage  $V_f$ , which is the rectified armature voltage of the excitation. Then

$$\Delta V_f = K_1 \Delta_{ie} \tag{10}$$

Where  $K_1$  is the rectified armature volt per ampere of exciter field current.

Taking Laplace transformation of the above two equations (9) and (10) and eliminating  $\Delta i_e(S)$ , we get

$$\Delta V_f(S) = \frac{\kappa_e}{1 + ST_e} \Delta V_R(S)$$
(11)

Where 
$$K_e = \frac{K_1}{R_e}$$
 and  $T_e = \frac{L_e}{R_e}$  (12)

Thus the transfer function of the exciter,  $G_e(S) = \frac{K_e}{1 + ST_e}$ 

The Generator field considering the field of a synchronous generator, using KVL;

$$\Delta V_f = R_f \Delta i_f + L_{ff} \frac{d}{dt} \left( \Delta i_f \right) \tag{13}$$

Taking Laplace transform;

$$\Delta V_f(S) = \left[ R_f + s^{L_{ff}} \right] \Delta i_f(S) \tag{14}$$

In the generator, internal emf and the field current are related as;

$$\mathbf{E} = \frac{\omega L_{fa \ i_f}}{\sqrt{2}} \tag{15}$$

Where  $L_{fa}$  is the mutual inductance coefficient between rotor field and stator armature. Thus;

$$\Delta i_f = \frac{\sqrt{2}}{\omega L_{fa}} \Delta E \tag{16}$$

Laplace transform of equation (16) gives

$$\Delta i_f(S) = \frac{\sqrt{2}}{\omega L_{fa}} \Delta \mathcal{E}(S) \tag{17}$$

Substituting equation (14), gives

$$\Delta V_f(S) = \frac{\sqrt{2}}{\omega L_{fa}} \left[ R_f + s^{L_{ff}} \right] \Delta E(S) \text{ Thus,}$$
  
$$\Delta E(S) = \frac{\omega L_{fa}}{\sqrt{2}} \frac{1}{R_f + s^{L_{ff}}} \Delta V_f(S)$$
(18)

From the above equation, the field voltage transfer ratio can be written as

$$\frac{\Delta E(S)}{\Delta V_f(S)} \approx \frac{\Delta |V|_{(S)}}{\Delta V_f} = \frac{\omega L_{fa}}{\sqrt{2}} \frac{1}{R_f + s^L ff} = \frac{K_f}{(1 + S_{Tf})}$$
(19)

Thus, the transfer function of the generator field is  $\frac{K_f}{1+ST_G}$ 

Where, 
$$K_f = \frac{\omega L_{fa}}{\sqrt{2} R_f}$$
 and  $T_G = \frac{L_{ff}}{R_f}$  (20)

#### 3.1 Simulink Model of AVR

Figure 3.2 shows an AVR system model in MATLAB simulink environment, where a combined simulation block model was constructed for the three (3) cases in other to obtain their respective step response.

The design configuration and automated PID turning tool of MATLAB was used to tune the PID controller for AVR. The PID controller is tuned to achieve better control action by further minimizing the error. This was performed by using MATLAB function "pidtune" (PID turning algorithm) for linear model and launching the PID tuner, using pidtool command. Upon launching the PID tuner the close loop step response of the system is obtained by automatically turning the gain constants  $K_{I}$ ,  $K_{P}$  and  $K_{D}$ .

The Parameters of the AVR and PID used for the simulation is shown in table 3.1 and table 3.2. The parameters were obtained in [9].

Table 3.1: AVR Parameter

Quality	Transfer Function	Gain	Time Constant
Amplifier	$K_{A} / (1 + T_{AS})$	$K_{A} = 10$	$T_{AS}=0.1$
Exciter	$K_{\rm E} / (1 + T_{\rm Es})$	$K_E = 1$	$T_{Es} = 0.4$
Generator	$K_c / (1 + T_{Gs})$	$K_{\rm G} = 0.8$	$T_{Gs} = 1.4$
Sensor	$K_{R} / (1 + T_{Rs})$	<b>K</b> <sub>R</sub> = 1	$T_{Rs} = 0.05$

**Table 3.2: PID Controller Parameter** 

Quality	Gain
PID Controller	$K_{p} = 1.0$
	$K_{I} = 0.25$
	$K_{\rm D} = 0.28$

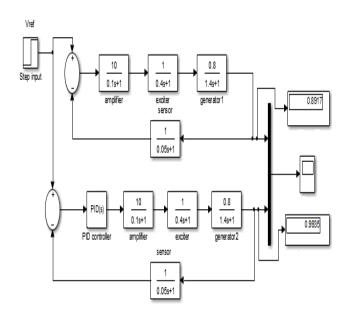


Figure 3.2 Generators with AVR and AVR with PID Controller

# 4. SIMULATION RESULTS AND DISCUSSION

Simulink model block was simulated and a plot for frequency deviation step response were obtained in Figure 4.1 for the generator AVR system. The AVR was simulated with and without PID controller, it was observed that generator 1 shows high frequency overshoot and oscillation than the generator 2, with PID controller which shows low frequency and minimum oscillation.

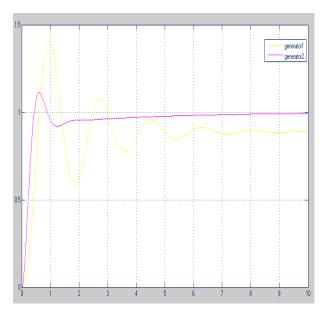


Figure 4.1 frequency deviation step response for the Generator System 1 and 2.

Figure 4.2 depicts the simulation result of the AVR response and the tuned PID controller response. The PID controller was tuned to further improve on the performance of the AVR loop. It can be seen from the plot that while using the tune PID controller the overshoot obtained is less as compared to the case when the PID controller was not tuned. Table 4.1 presents the performance of the tuned response of the AVR system with PID controller. The settling time of the AVR with PID controller is less, which is recorded at 1.44 s as against 5.54 s for the AVR without PID.

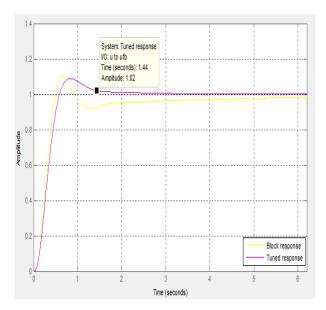


Figure 4.2 Voltage step response for AVR systems with tuned PID controller

	Tuned	Block
Rise time (seconds)	0.372	0.305
Settling time (seconds)	1.44	5.54
Overshoot (%)	8.84	10.5
Peak	1.09	1.1
Gain margin (rad/s)	18 @ 13.5	14.7 @ 12.2
Phase margin (rad/s)	60 @ 3.23	54.1 @ 4.03
Closed-loop stability	Stable	Stable

Table 4.3 Performance metrics of tuned AVR loop

#### 5. CONCLUSION

The reliability of generating system is determined by its ability for maintaining voltage and frequency within permissible limit. A generator was modeled to determine its terminal voltage step response, where the system was simulated considering two different configuration. It can be concluded that the generating system with PID controller shows satisfactory performance. It is found that turning of PID controller improves performance of systems. The settling time and rise time of the response acquired by the AVR system with tune PID controller is found to be less as compared to response acquired by the AVR system without PID controller.

Further work should be carried out using artificial intelligence technique such as; Fuzzy logic, Neural Network, Adaptive Neuro-Fuzzy Inference system e.t.c to further compare the result obtained in this work.

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