

3 Phase Monitoring and Control Control Systems based on Labview

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ABSTRACT

Large power consumers or consumers who have their own distribution substations, such as Hotels, Offices, Shopping Centers, Industries, etc. Installation is connected with 3 phase electricity, which includes phase R, phase S and phase T. In planning and installing electrical installations carried out by installer (Electrical expert) always refers to the design of loads connected in R, S and T phases, which are balanced (equally large), but in the operation (ON / OFF) of the electrical load the balance of the load cannot be adjusted for each phase, because operation (ON / OFF) adjusted to the needs of electrical equipment installed in each phase, so that the load is not balanced. Unbalanced loads in each phase can cause electrical power losses. Electricity losses will be even greater, if additional loads or the development of electrical installations are carried out without regard to the initial design of installation. In this study a simulation of a monitoring and control system will be created automatically as a study material to minimize the occurrence of phase load imbalance. The control system will work automatically to monitor and control supply to the load. Besides that, this control system can also detect if one or two phases are lost (OFF), where the system will provide information to the user in the form of an alarm, which shows that there has been a loss of phase, so that users can carry out the overall power outage. avoid the occurrence of large power losses. The method used in making the system is a method of research and development, with the stages including the study of literature, field studies to obtain data relating to the making of the system. The next stage is designing software that is simulated through the Lab View program. The next step is to test the work of the control system based on a system algorithm made through program simulations. The results of the tests were carried out, when the system was not burdened and burdened, for example the load detected in Phase R was 1.01 Ampere, in Phase S as basic as 1.5 Amperes and in Phase T of 1 Ampere, the system would automatically move one of the electricity lines from Phase R to one of the paths of Phase T, so that the overload in Phase R is overcome by Phase T. Besides that, when indicated there is one phase missing, the system will inform the alarm indicator and display on the monitor.

Keywords

Simulation, control system, load control, Labview

1. INTRODUCTION

Unbalanced loads in phase R, S and T will make a current flow in the neutral point. The greater the unbalanced load will also make the greater the current flowing at the neutral point. This makes electric power wasted or power losses occur. In addition to power losses, this unbalanced load will contribute to the distribution of power by the transformer to a lesser extent. To minimize the occurrence of electrical power losses

caused by the operation of unbalanced loads and over time the development of electrical installations takes place without regard to the initial installation electrical installation planning, and if there is a loss of one or two electrical phases, a control system that can detect and regulate load in phase R, phase S and phase T are relatively balanced, besides that the Control system created can also provide information to the user if there is a loss of one or two phases in order to avoid power losses. The integrated system in making unbalanced load loss control systems uses the Lab View and Arduino Uno yang controllers to function as the overall working regulator of the system [1] [2], current sensors that function as detecting changes in load current [3], voltage sensors that function to detect phase loss [4], a program algorithm (flowchart) that functions for steps to be taken in making programs and system modeling [5] [6]. To run the system, it takes a program that is embedded into the Arduino Uno microcontroller, where the software used to create the program is the IDE View Lab, with reference to the flowchart made [7].

2. METHODOLOGY

In producing an automatic system model of unbalanced power losses, a system design process is carried out which includes designing a system model, designing hardware, designing software, testing the system through a simulation program to obtain data in connection with system work.

2.1 Designing a System Model

The design of the system model aims to describe and determine the components that support the work of the control system of the automatic control system, the electrical power losses are unbalanced in the phase R, S, T.

The system model is shown in Figure 1, with the job description as follows:

1. Flow Sensor, serves as a detector of changes in load current on the 3 phase network.
2. The phase detection sensor is lost, it functions to detect phase loss from the 3 phase system.
3. Lab View and Arduino Uno controllers, which function as media controllers in the process of regulating load supply, using 2 controllers that function as slave masters.
4. Relay, functions as a breaker and electrical supply connector to load.

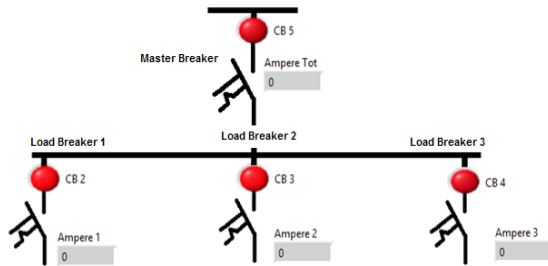


Fig 1: Wiring Diagram

2.2 Design and Manufacture of R, S, T load control devices

The design of the system simulation is made using the Lab View simulation program, by arranging the supporting components of the control system according to their functions and needs in the system. Figure 2 shows the system design created. When the load is detected in a balanced state, where the amount of electricity consumption is detected through the current sensor from each phase, both phase R, phase S and Phase T are equal

3. RESULT AND DISCUSSION

3.1 Testing When Phase R, S and T are not loaded

Figure 2 shows the test results when the system detects that inter-phase loads are not loaded. When the load is detected in a balanced state, where the amount of electricity consumption is detected through the current sensor from each phase, both phase R, phase S and Phase T are equal

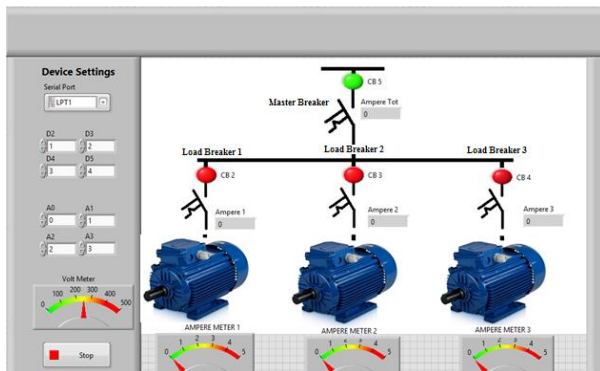


Fig 2: system testing without load

3.2 Testing When Phase R, S, T is currently loaded

When the system detects that the load on phase T is greater than the load on phase R, while the load on phase R is greater than the load on phase S, then the system will automatically deactivate the relay on one of the lines to the load from phase T, and be replaced with a relay reserves of phase S to supply the load on the replaced T line, to keep the system relatively balanced. In addition, the system will inform the monitor that one path to the load of phase T has been replaced by one of the S phase tie lines to overcome the overload of the T phase. Figure 3 shows a test when phase T is greater than phase R and phase R is more big from phase S.

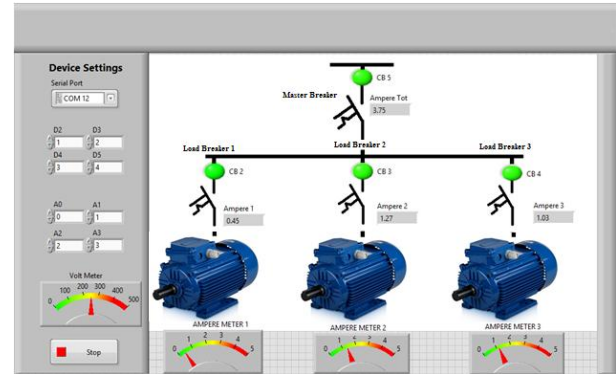


Fig 3: system testing when burdened

3.3 Measurement of Electric Current

The process of testing the current sensor circuit shows that there are 4 test points, where the output voltage depends on the change in electric current from the load. The test is carried out by assuming that the air conditioning equipment used is 1/2 PK capacity. The test results of the current sensor circuit are shown in Table 1.

Table 1. measurement results of used current

he results of testing the current sensor in Table 1 show that there are 4 stages of current testing carried out. Current testing is carried out using an ammeter to measure the amount of current consumed by air conditioning.

a. The first test is carried out when the AC is inactive, where the current is 0 Ampere. The output of the current sensor that is carried out using the Volt meter (V1) measuring instrument is 2.49Volt. From the results of the test there is a difference of 0.01 volt from the output data issued by the factory where the data is 2.5 volts when the sensor is not banned, so the magnitude of the error between factory data and the test data is:

$$\begin{aligned} \text{Error (\%)} &= \frac{\text{sensor data current} - \text{measurement data}}{\text{sensor data current}} \times 100 \% \\ &= \frac{2,5 - 2,49}{2,5} \times 100 \% = 0,4 \% \end{aligned}$$

Voltage testing is done by using a V2 measuring device at the output after passing through the voltage divider circuit and rectifier circuit showing the number 1.24Volt. Theoretically the output voltage of the voltage divider circuit is as follows:

$$V_{\text{Output}} = \frac{R_2}{R_2 + R_1} \times V_{\text{in}}$$

Where is the amount of $R_1 = R_2 = 1 \text{ k}\Omega$, so that

$$\begin{aligned} V_{\text{Output}} &= \frac{1000}{1000 + 1000} \times 2,49 \\ &= 0,5 \times 2,49 \\ &= 1,24 \text{ Volt.} \end{aligned}$$

The amount of error between the results of calculations and the test results is as follows:

$$\begin{aligned} \text{Error (\%)} &= \frac{\text{calculation data} - \text{measurement data}}{\text{calculation data}} \times 100 \% \\ &= \frac{1,24 - 1,24}{1,24} \times 100 \% \\ &= 0 \% \end{aligned}$$

The voltage test carried out by using a V3 measuring device at the output after passing through the op-amp amplifier circuit

shows a number of 2.48Volt. Theoretically the output of this amplifier circuit is as follows:

$$V_{Output} = -\frac{R_f}{R_{in}} \times V_{in}$$

Where :

Rf = Feedback resistor (amounting to 20 kΩ)

Rin = Resistor input (for 10 kΩ)

Vin = Input voltage (equal to 1.24 Volt)

So that:

$$\begin{aligned} V_{Output} &= -\frac{20000}{10000} \times 1,24 \\ &= -2 \times 1,24 \\ &= -2,48 \text{ Volt} \end{aligned}$$

Because the ADC only accepts positive voltage, then the second op-amp circuit with a configuration as 1-time gain will reverse the negative voltage polarity produced by the first op-amp into a positive voltage of 2.48Volt. From the test results and the calculation results there is an error of:

$$\begin{aligned} \text{Error (\%)} &= \frac{\text{calculation data} - \text{measurement data}}{\text{calculation data}} \times 100 \% \\ &= \frac{2,48 - 2,48}{2,48} \times 100\% \\ &= 0 \% \end{aligned}$$

b. The second test is carried out when the air conditioner is active and the compressor is off, where the current is 0.7 Ampere. The output of the current sensor that is carried out using a Volt meter (V1) measuring instrument is 2.62Volt. Theoretically, the output voltage of the current sensor is equal to:

$$\begin{aligned} V_{Output} &= 2,5 + (0,185 \times I) \\ &= 2,5 + (0,185 \times 0,7) \\ &= 2,63 \text{ Volt.} \end{aligned}$$

The amount of error between the calculation data and the test data is:

$$\begin{aligned} \text{Error (\%)} &= \frac{\text{calculation data} - \text{measurement data}}{\text{calculation data}} \times 100 \% \\ &= \frac{2,63 - 2,62}{2,63} \times 100\% \\ &= 0,4 \% \end{aligned}$$

Voltage testing is done by using a V2 measuring device at the output after passing through the voltage divider circuit and rectifier circuit showing the number 1.31Volt. Theoretically the output voltage of the voltage divider circuit is as follows:

$$V_{Output} = \frac{R_2}{R_2 + R_1} \times v_{in}$$

Where is the amount of R1 = R2 = 1 kΩ, so that

$$\begin{aligned} V_{Output} &= \frac{1000}{1000 + 1000} \times 2,62 \\ &= 0,5 \times 2,62 \\ &= 1,31 \text{ Volt.} \end{aligned}$$

The amount of error between the results of calculations and the test results is as follows:

$$\text{Error (\%)} = \frac{\text{calculation data} - \text{measurement data}}{\text{calculation data}} \times 100 \%$$

$$\begin{aligned} &= \frac{1,31 - 1,31}{1,31} \times 100\% \\ &= 0 \% \end{aligned}$$

Testing the voltage carried out using a V3 measuring device at the output after passing through the op-amp amplifier circuit shows a number of 2.62Volt. Theoretically the output of this amplifier circuit is as follows:

$$V_{Output} = -\frac{R_f}{R_{in}} \times V_{in}$$

Where :

Rf = Feedback resistor (amounting to 20 kΩ)

Rin = Resistor input (for 10 kΩ)

Vin = Input voltage (equal to 1.31 Volt)

So that:

$$\begin{aligned} V_{Output} &= -\frac{20000}{10000} \times 1,31 \\ &= -2 \times 1,31 \\ &= -2,62 \text{ Volt} \end{aligned}$$

Because the ADC only accepts positive voltage, then the second op-amp circuit with a configuration as 1-time gain will reverse the negative voltage polarity produced by the first op-amp into a positive voltage of 2.62Volt. From the test results and the calculation results there is an error of:

$$\begin{aligned} \text{Error (\%)} &= \frac{\text{calculation data} - \text{measurement data}}{\text{calculation data}} \times 100 \% \\ &= \frac{2,62 - 2,62}{2,62} \times 100\% \\ &= 0 \% \end{aligned}$$

c. The third test is carried out when the air conditioner is active and the compressor is on to reach the appropriate temperature. with a set point, where the current is 1.7 Ampere. The output of the current sensor that is carried out using a Volt meter (V1) measuring instrument is 2.82Volt. Theoretically, the output voltage of the current sensor is equal to:

$$\begin{aligned} V_{Output} &= 2,5 + (0,185 \times I) \\ &= 2,5 + (0,185 \times 1,7) \\ &= 2,81 \text{ Volt.} \end{aligned}$$

The amount of error between the calculation data and the test data is: 0%

The voltage test carried out by using a V3 gauge at the output after passing through the op-amp amplifier circuit shows a number of 2.81Volt. Theoretically the output of this amplifier circuit is as follows:

$$\begin{aligned} V_{Output} &= -\frac{20000}{10000} \times 1,4 \\ &= -2 \times 1,4 \\ &= -2,8 \text{ Volt} \end{aligned}$$

Because the ADC only accepts positive voltage, then the second op-amp circuit with a configuration as 1-time gain will reverse the negative voltage polarity produced by the first op-amp into a positive voltage of 2.8 Volt. From the test results and the calculation results there is an error of:

$$= \frac{2,8 - 2,81}{2,78} \times 100\%$$

= - 0,35 %

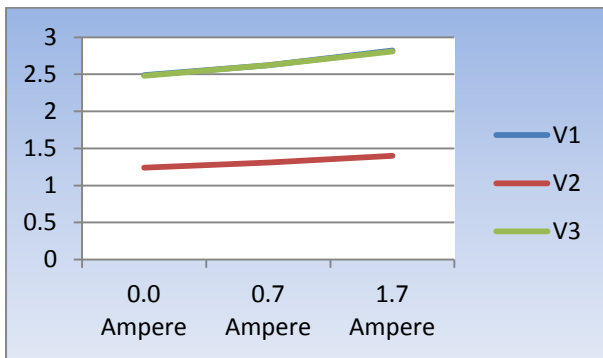


Fig 4: Voltage measurement chart for current

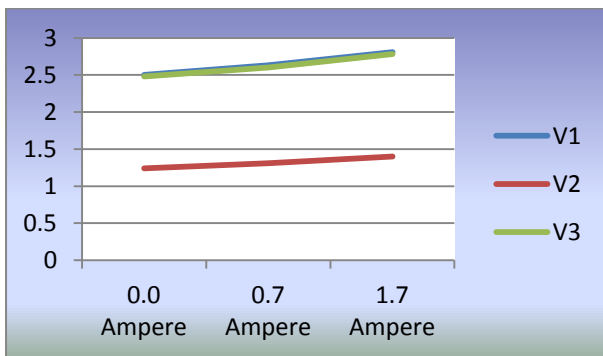


Fig 5: Graph of voltage calculation with current

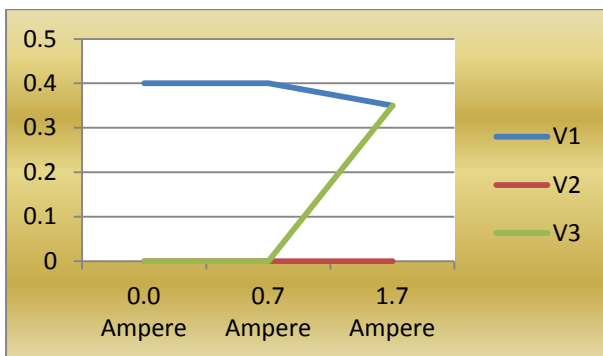


Fig 6: Error Graph

4. CONCLUSIONS

Based on the results of 70% of activities, it can be concluded as follows: Has produced hardware schematics from the 3 phase load balancing control system automatically to avoid power losses, through the stages of block diagram design, hardware design and hardware making by integrating it through a proteus simulation program that represents the system real control. Software has been produced for the work requirements of the 3-phase load balance control system to avoid power losses, through the stages of software design in the form of flow diagrams and program creation through Arduino IDE. The remaining work to be done is 30% which includes installing the generated program into the Arduino Uno controller in the proteus program simulation, then testing the functional work of the 3 phase load balance control system automatically to avoid power losses, to get data related to the work function of the control system.

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