Abstract

In this paper, we describe a transmitter scheme which uses Shortened Pulse Position Modulation (SPPM). A modulator based-on hardware is proposed for SPPM technique. The transmitter scheme is designed considering wireless optical communication systems. We use Field Programmable Gate Arrays (FPGA) board to design proposed system. It is shown from simulation results that proposed architecture efficiently generates SPPM signal. Additionally, we implemented on real-time FPGA board since we show that proposed structure has practical scheme.

References

Implementation of Shortened Pulse Position Modulation for Optical Communication Systems


Index Terms

Computer Science
Communications

Keywords

Shortened Pulse Position Modulation (SPPM), Field Programmable Gate Arrays (FPGA), wireless optical communication.