Abstract

In this paper, a barrel shifter is a specialized digital electronic circuit with the purpose of shifting an entire data word by a specified number of bits by using combinational logic and sequential logic used. Multiplexer based 8-bit barrel shifter circuit is implemented using the hardware description language – Verilog. The proposed barrel shifter architecture implementation shows reduction in power consumption.

References

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Index Terms

Computer Science
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Keywords

Multiplexer, verilog HDL, power, pulsed latches, microwind.