Abstract

Reconfigurable computing has become an essential part of research in the domain of modern computing paradigms. Reconfigurable computing approach integrates both, the performance and flexibility gaining aspects on a single computing system. The computational performance of such kind of systems is crucially dependant on the configuration overheads caused by
Performance Enhancement Techniques for Modern Reconfigurable Computing Systems

configuration management unit. Performance of the configuration management unit greatly accelerates the computational power of reconfigurable computing system. There are a large number of control and management techniques which can be used to improve this technology. This research paper presents a comprehensive analysis of existing performance enhancement methodologies in practice. The paper also point outs the different aspects of configuration management for critical analysis and further optimization.

Reference

- M. Aqeel Iqbal, Shoab A. Khan and Uzma Saeed Awan, “Computational Unit Design For High Speed Reconfigurable Processors”, Published in International Journal of Intelligent Information Technology Application (IJITTA), Volume-02, No-05, PP 229-236, October-2009, Published by Engineering Technology Press. ISSN: 1999-2459.

Index Terms

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Key words

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