Abstract

Low power device design is now a vital field of research due to increase in demand of portable devices. This research paper proposes novel design of 8-transistor latch. Design comparison with the conventional design is performed at 65nm and 45nm to show technology independence. Comparative simulation results show that area and power efficient latch design is better choice for portable applications.

References


Index Terms
Computer Science Integrated Circuits

Keywords
A Novel Latch design for Low Power Applications

Level converting Flip Flop  Portable Applications  Latch  Sub-threshold Region  and  Low Power applications