Abstract

In this paper, the implementation of DSP modules like FIR Filter and FFT based systems are designed and implemented. The design is based on high performance FPGA “Cyclone II” and implementation is done after functional and timing simulation. The simulation tool used is ModelSim. The tool for synthesis and implementation is Quartus II. The experimental results shows the functional and timing analysis for all the DSP modules carried out using high performance synthesis software from Altera.

References


**Index Terms**

Computer Science  
Signal Processing

**Keywords**

Simulation  
FPGA  
Code Conversion