Abstract

Multistage interconnection networks (MINs) are used to connect N inputs to N outputs. They are mainly used to connect processor to processor and for processor to memory in distributed and shared memory environment. The MINs are broadly divided into three categories Blocking, Non Blocking and Rearrangeable networks. A new improved Arbitrary size Benes network has been proposed in this to improve the permutation capabilities and to reduce the cost of existing Arbitrary Size Benes Network.

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Index Terms

Computer Science  Networks

Keywords

Improved Arbitrary Size Benes Network  Arbitrary Size Benes Network Path  Improved Arbitrary Size Benes Network Path

Arbitrary Size Benes Network Path Length
Improved Arbitrary Size Benes Network Path Length