Abstract

Multistage interconnection networks (MINs) are used to connect N inputs to N outputs. They are mainly used to connect processor to processor and for processor to memory in distributed and shared memory environment. The MINs are broadly divided into three categories Blocking, Non Blocking, and Rearrangeable networks. A new improved Arbitrary size Benes network has been proposed in this to improve the permutation capabilities and to reduce the cost of existing Arbitrary Size Benes Network.

References


Chi-Ping Lee, Jiun-Shiou Deng, Ming-Feng Lu and Yang-Tung Huang, "Benes Networks with High Contrast Ratio Holographic Optical Switching Elements", Proceedings of the 4th WSEAS/IASME Int. Conf. on System Science and Simulation in Engineering, Tenerife, Spain, December 16-18, 2005.


Index Terms

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Keywords

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Improved Arbitrary Size Benes Network Path

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