Abstract

The non-linearities like saturation, dead zone and relay etc. of electronic components like op-amp leads to limit cycles in the electronic system. Limit cycles describe the oscillations of non-linear electronic systems. This paper examines the development of a graphical technique, leading to the use of computer graphics, for systematic analysis of limit cycles in Second and Higher Order Electronic Filter Systems. Its accuracy has been sustained by comparing with the results from digital simulation.

References


Index Terms
Computer Science                Signal Processing

Keywords
Phasor Diagram   Limit Cycles    Electronic Filter System