Abstract

Implementation of Original CORDIC, Control CORDIC and Angle Recoding CORDIC of 16-bit, 24-bit and 32-bit fixed point number have been done in this paper. VHDL is used to modelled these architectures. Original CORDIC, Control CORDIC and Angle Recoding CORDIC are synthesized and targeted for Xilinx Virtex 5 FPGA and the results calculated for 16-bit and 24-bit has shown satisfactory improvement in speed.

References

- Y. Hu and S. Naganathan, "An Angle Recoding Method for CORDIC Algorithm"
Implementation of High Speed Fixed Point CORDIC Techniques


**Index Terms**

Computer Science

Algorithms

**Keywords**

CORDIC algorithm

VHDL

Fixed Point representation