Abstract

Floorplan representation is a fundamental issue in designing a VLSI floorplanning algorithm as the representation has a great impact on the feasibility and complexity of floorplan designs. This survey paper gives an up-to-date account on various non-slicing floorplan representations in VLSI floorplanning.

References


Non Slicing Floorplan Representations in VLSI Floorplanning: A Summary

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Index Terms

Computer Science  Automation

Keywords

VLSI floorplanning  non-slicing floorplan