Abstract

The paper focuses on the efficient FPGA implementation of the baseband receiving functions for a Reconfigurable Data-Link (RDL). A binary CP-FSK case study is considered to show the model-based approach. The design is then implemented and tested on low cost FPGA.
NY, November 1997
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Index Terms

Computer Science Communication Systems

Keywords

Synchronization Software Defined Radio CP-FSK Demodulation Model-based Design