Abstract

The paper focuses on the efficient FPGA implementation of the baseband receiving functions for a Reconfigurable Data-Link (RDL). A binary CP-FSK case study is considered to show the model-based approach. The design is then implemented and tested on low cost FPGA.

References

- Rice Michael, Padilla Marc, Nelson Brent, "On FM De-modulators in Software Defined Radios Using FPGAs", Grant no. 0801876, I/UCRC Program of the National Science Foundation, Provo, Utah, October 2009
NY, November 1997
- Erup Lars, Gardner Floyd, Harris Robert, "Interpolation in Digital Modems – Part II: Implementation and Per-formances", IEEE Transactions on Communications, vol. 41, no. 6, June 1993, pp. 998 – 1008
- Comblock datasheets: www.comblock.com

Index Terms

Computer Science          Communication Systems

Keywords
Synchronization  SoftwareDefined Radio  CP-FSK Demodulation  Model-based Design