Abstract

With progressive FPGA technology, XILINX required a need for new format to provide needful assistance as a part of their tool set for design constraints. In order to achieve the same, XDC (Xilinx Design Constraints) was introduced. In this paper, we describe a generalized technique to integrate XDC in synthesis tool using YACC. Proposed system not only tokenizes the input XDC commands but syntactically and semantically validates them to generate desired lexeme. This paper shows the parsing mechanism to generate desired lexeme which can be used by several synthesis subsystems for further computations.

References


Index Terms

Computer Science  Algorithms

Keywords

Syntax  Semantic  YACC