Abstract

This paper enumerates high speed design of RS & D- flip-flop using AlGaAs/GaAs MODFET. The proposed Flip Flop is having less number of transistors than existing designs. Simulation results show lowest average power and least delay than existing designs. This Flip-Flop having less number of transistors. It can be efficiently used in VLSI ICs. In the verification by simulation, the proposed flip-flops appear to have better speed of operation. It is simple and suitable to SPICE simulation of hybrid digital ICs.

References

Design of RS and D-Flip-Flop using AlGaAs/GaAs MODFET Technology


Index Terms
Computer Science
Circuits And Systems
Keywords
Flip-Flop  MODFET  delay  PDP  power consumption.