Abstract

The Appearance of radix-22 was a milestone in the design of pipelined FFT hardware architectures. Later, radix-22 was extended to radix-2K. In the feed forward architectures radix-2K can be used for any number of parallel samples which is a power of two. Indeed, it is shown that feed forward structures are more efficient than feedback ones when several samples in parallel must be processed. As a results shown that the proposed designs are efficient both in area and perform ace, being possible to obtain throughputs of the order of GSamples/s as well as very low latencies.
An Efficient Multi-Path Delay Commutator Architecture


Index Terms

Computer Science Communications

Keywords

Fast Fourier Transform (FFT) Radix-2k Multipath Delay Commutator (MDC) Pipelined Architecture

Very Large-Scale Integration (VLSI).