Performance Analysis of Priority Scheme in ATM Network

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ABSTRACT

The asynchronous Transfer Mode (ATM) is the widely used packet switching and connection oriented technology that meets diverse services and performance requirements of real time applications using the broadcast . An ATM network is suitable for multimedia services with different traffic flow characteristics and quality of services (QoS) requirements. In order to meet the required QoS of each traffic type, some types of priority disciplines are needed in packet switching to increase the utilization of switches in ATM networks. This paper describes a buffer management scheme using two different prioritized service disciplines in order to improve the QoS in terms of cell delay time for each traffic class and to reduce the cell loss rate of loss sensitive class in an ATM network. The performance analysis and comparative study of these priority schemes have been presented.

1. INTRODUCTION

broadband integrated services digital network (B- ISDN) is designed to support and accommodate a wide variety of service demands such as voice, data, video and their possible combinations in a flexible manner, both at the level of individual and the system as a whole [1,2]. Asynchronous Transfer Mode (ATM) is a cell oriented transfer mode based on fixed size calls and accepted universally as the transfer mode of choice for B-ISDN. ATM technology is suitable to support high speed multimedia services with diversified traffic flow characteristics and QoS requirements because it provides flexibility in bandwidth allocations through the assignment of fixed length packets. In an ATM network, various types of traffic are statistically multiplexed to efficiently utilize the network resources. However, the excessive use of the bandwidth may cause traffic dependent OoS deteriorations, such as cell delay and cell loss at the buffer for different traffic classes [18,19].

In order to maintain the QoS of ach traffic type, some types of priority schemes are required to use in scheduling process at switching node in an ATM network. These priority schemes treat each traffic class according to its QoS requirements and support – integrated services [3,10,15,16]

In this paper two types of priority schemes namely – arrival Rate Based State Dependent (ARBSD) priority scheme and the Relative State Dependent (RSD) Priority schemes have been analyzed for the scheduling of an output buffered ATM switch [3,4]. In the ARBSD priority scheme the number of cells of each traffic class in the buffer and its corresponding arrival rate to decide, which class is selected for service in the next time slot? In the RSD priority scheme, the decision of next services is based only on the number of cells in each class.

The service discipline of the ARBSD priority scheme is described as follows: Suppose the number of delay sensitive cells and loss sensitive cells in the output buffered switch at time slot n are Q_d (n) and Q_1 (n) and the average arrival rate for the delay sensitive class and the loss sensitive class are P_d and p_1 respectively. Let R be an adjustable parameter, which is used to control the delay time of each traffic class.

If the condition $[(Q (n)/p1)/Q_d (n)/p_d)] = R$ is held, the delay sensitive cell is selected for service otherwise loss sensitive cell is selected for service [3,4,17].

The RSD priority scheme is described as: If the condition $[(Q (n)/Q_d (n)]=R$ is held the delay sensitive class is selected for service, otherwise, the loss sensitive class is selected for services [3,4,17,19].

2. SYSTEM DESCRIPTION

System, considered in this paper, is assumed to be an N x N ATM switch with output buffer as shown in fig. 1. Each output port has a buffer of size B, where cells arrive in batches. From QoS point of view, services can be classified into two categories, i.e., delay sensitive (DS) services and loss sensitive (LS) services. The DS services are those, which have stringent delay and delay variation requirements (e.g. voice and video (real time)services . The LS services have stringent cell loss requirement (e.g. compressed video, data transfer). So we have cell delay. Cell delay variation and cell loss rate as QoS parameters.

The buffer architecture, as shown in fig. 2. is used to reduce the cell loss rate for los sensitive class. Two pointers are used to po9int the position of the arrived priority cells. The arriving DS cells are placed into the buffer from right to left by right pointer, whole the arriving LS cells are placed into the buffer from left to right by left pointer respectively. When the buffer is full, pointer are next to each other. Under this situation, any DS cell arrival is discarded, but a LS cell arrival will be stored by replacing a DS cell in the buffer.

3. PERFORMANCE ANALYSIS

In this section the average delay time and cell loss rate for each class of traffic using the above –discussed two priority schemes are analyzed. We assume that cell arrival at each input is used as a heuristic approach to Poisson process begins with Bernoulli trials with traffic load P[17]. Each incoming cell is equally appearing at

any of the N output po9rts. Here, traffic load p means arrival rate for N input lines. So arrival rate or traffic load for each input line becomes (p/N)

We consider only one port j to simplify the analysis because the performance of each output buffer in the switch is the same. The total number of cells appeared for output port j at time slot n is denoted by A (n). For a switch operating N times the speed of the input line, the batch arrival size for output port j is limited to N. The Probability of k successes in N trials [17].

3.1.1 ANALYSIS FOR THE ARBSD AND RSD PRIORITY SCHEMES

Let A_d (n) and A_1 (n) demote the number of delay sensitive cells reaching output port j in time slot n.

Let P_d be the probability that an incoming cell is a delay sensitive one. The average arrival rate for delay sensitive class is p P_d and the arrival rate for loss sensitive class is p (1-p)

Let $d_k = \Pr[A_d(n) = K]$

K delay sensitive arrivals would mean that there are at least k total arrivals at the output port.

$$\mathbf{d}_{k} = \underbrace{\sum_{k \leq N} \mathbf{c}_{m}}_{\substack{m = k \\ 0}} \left(\begin{array}{c} m \\ k & p_{d} \end{array}^{m-k} (1 - P_{d})^{m-k} \right)$$

k loss sensitive arrivals would mans that there are at least k total arrivals at the output port

Let Q_1 (n) denote the number of delay – sensitive cells and loss – sensitive cells in the buffer at the end of the nth time slot. In out case, if the queue is not empty in the current time slot then one of the cells will be transmitted in the next time slot. Thus we have

Q₁ = mine { B, Q₁ (n - 1 + A_{1 (n)}-1 } if

$$\left[\frac{Ql(n-1)/pL}{Qd(n-1)/pd}\right] > R$$
mine { B, Q1 (n - 1 + A1 (n) } otherwise

$$Q_{d} = \min \{ B, Q1 (n-1) + A1 (n) -1 \} \text{ if}$$

$$\left[\frac{Q1(n-1)/pL}{Qd(n-1)/pd} \right] \leq R$$

$$\min \{ B, Q1 (n-1) + Ad (n) \} \text{ otherwise}$$

$$Q_{d} = \min \{ B, Q1 (n-1) + A1 (n) -1 \} \text{ if}$$

$$\left[\frac{Q1(n-1)/pL}{Qd(n-1)/pd}\right] \leq R$$

$$\min \{ B, Q1 (n-1) + Ad (n) \} \text{ otherwise}$$

From these equations we find that the system state $[Q_d (n), Q_1 (n)]$ at n th time slot depends only on the system state at the $(n-1)^{th}$ time slot $[Q_d (n), Q_1 (n)]$ at n th, thus a two dimensional Markov Chain forms. The state transition probability between any two system states is denoted as T (j, m; I, 1) and can be defined as

$$\begin{array}{c} Q_{1}\left(n\right)=,\ J\ Q_{1}\left(n\right)=m\mid \\ T\ (j,\ m;\ i,\ 1\)=P\ [Q_{1}\left(n-1\right)=i,\ Q_{1}\left(n-1\right)\] \end{array}$$

The steady state distribution P (i,m) is defined as P (j,m)= lim [Qd (n) =j Q1(n)=m]

The stationary distribution P (I,m) is obtained by solving the state balance

Equation, P (j,m)=
$$\sum_{J=0}^{D} \sum_{j=0}^{D} T$$
 (j, m: I, I P (I,I)
And the normalizing equation

$$B B - 1$$

$$\sum_{j=0}^{\infty} \sum_{m=1}^{p(j, m)} P(j, m) 1$$

By solving the steady state P(i,m) using these above equations, we obtain the steady state distribution for delay sensitive class and loss sensitive class Pd (j) and P1 (m) respectively as,

$$P_{d} = \sum_{m=0}^{B-1} P(j, m)$$

$$B - m$$

$$P_1 = \sum_{j=0}^{\infty} (j, m)$$

Now, average number of delay sensitive cells in the queue is

$$\overline{Q}_{d} = \sum_{\substack{j=1}}^{B} jP_{d}(j)$$

Similarly the average number of loss sensitive cells in the queue is

$$Q_{d} = \sum_{m=1} mP_1(j)$$

Now we proceed to find the average number of loss sensitive cells lost in a time slot, the cell loss rate and the waiting time for the same.

Let, there be m loss sensitive cells in the queue and K loss sensitive arrivals in the next time slot. If a loss sensitive cell is not selected for transmission, then in the next time slot, there will be $[k - (B-m)]^+$ loss sensitive cells lost. If a loss sensitive cell is selected for transmission at the next time slot, there will be $[k-(B-m+1)]^+$ loss sensitive cells lost. Thus, the

Average number of loss sensitive los in a slot, denoted as L_1 is given as

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$$L_1 = \sum_{\substack{j \\ pd}} \sum_{R < \frac{m}{m}} P(j,m) \sum_{k=1}^{N} 1_k - (B - M + 1)]$$

$$\frac{\sum \sum P(j,m) \sum l_k - (k - (B - m))}{pd} \ge \frac{m}{m} = B - m + 1 N$$

So the cell loss rate for loss sensitive traffic class is

$$P_1^{\text{loss}} = \frac{L1}{P1}$$

4. RESULTS AND DISCUSSION

In this section, we examine the performance of an output buffered ATM switch using the static Priority scheme and the ARBSD priority schemes and the RSD priority scheme traffic parameters namely as buffer size, the number of ports and the probability of the incoming cells, Figure 3. Shows the delay time ratio versed arrival rate for the static priority scheme and the ARBSD, RSD priority schemes. The buffer size is 20, the number of output port is 16, and the probability that an incoming cell is delay - sensitive equals to 0.5 ($P_d = 0.5$). The control parameter R in the analyzed schemes is equal to 1, 2, and 4 respectively. The delay time ratio in the ARBSD priority scheme is the same as that in the RSD priority scheme because P_d (Probability of delay sensitive cell arrival) is 0.5. In the static priority scheme, the delay time ratio increases with arrival rate, but in the ARBSD in case of R = 1ARBSD, and then it gradually approaches the value of the control parameter R when the arrival rate approaches 0.9. Mean while, the delay time ratio in both the ARBSD and the RSD priority scheme increase as control parameter R increases. When the control parameter R equals 1, the delay time ratio is still greater than 1. This implies that the average delay time for delay sensitive class in smaller than the average delay time for loss sensitive traffic class. . Figure 4. shows the delay time ratio verses arrival rate with P_d =0.95. In this case the most of the traffic is delay sensitive. We find that the delay time ratio for the ARBSD priority scheme is much smaller than that in the static priority scheme . The delay time for loss- sensitive transitive traffic class in the ARB?SD priority scheme is smaller than that in the RSD priority scheme. The reason lies in that the arrival rate for the delay sensitive class P_d is much larger than that of loss sensitive class.

Figure . 5 (a0 & (b) shows delay time for delay sensitive and loss sensitive classes of traffic respectively in the ARBSD priority scheme. The average delay time for the delay sensitive class in the ARBSD priority scheme is larger than that in the static priority scheme ARBSD priority scheme is smaller than that in the static priority scheme. Thus the improvement of the delay for loss sensitive traffic class is at the expense of the delay time delay – sensitive traffic class.

Figure 6. shows delay time ratio versus probability P_d for arrival rate equal to 0.9. the delay time ratio in the ratio in the RSD priority scheme increases monotonically. The delay time ratio in the ARBSD priory schemes decreases with P_d . Figure 7. (a) & (b) show cell loss rate versus buffer size for loss sensitive and delay sensitive traffic classes with P_d =0.5. buffer soze required to get the cell loss rate for loss sensitive class in ARBSD priority scheme with R = 1 is between 5 to 10. Buffer size is between 10 to 15 with R =2 and with R =4, buffer size is between sized is between 15to 20,

In the case of static priority scheme buffer size required to achieve the loss rte for loss sensitive traffic class is between 25 to 30.

5. CONCLUSION

We discussed and analyzed a buffer management scheme for ATM networks to allocate two different classes of traffic, which require different QoS, is simulated offer random traffic. The delay time of random traffic in the ARBSD priority scheme and the RSD priority scheme are controlled by the control parameter R. When the control parameter R decreases, the average delay time of the loss sensitive traffic class decreases at the expanse of average delay time of the delay sensitive traffic class for both schemes.

When most of the traffic is loss sensitive, the average delay time of loss sensitive traffic class decreases negligibly, but the average delay time for delay sensitive class increases slightly. Therefore it is less beneficial to employ the ARBSD priority scheme and the RSD priority scheme When most traffic is delay sensitive, the average delay time of loss sensitive traffic class decreases much at the expanse of average delay time of the delay sensitive traffic class. So the reduce the starvation problem of the static priority scheme. It is beneficial to use the ARBSD priority scheme and the RSD priority scheme

In the ARBSD priority scheme, the improvement of delay time is better than in the RSD priority shame. Compared to the RSD priority schema, the delay time in the ARBSD priority scheme is less sensitive to the probability P_d Thus the ARBSD priority scheme is favorable when there are different kinds of traffic mixture.

The cell loss rate of loss sensitive traffic class in the ARBSD priority scheme is better than in the static priority scheme

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Fig. 3 Delay Time Ratio Vs Arrival Rate for Different Priority Schemes





Fig. 4. Delay Time Ratio Vs Arrival Rate for different Priority schemes



Figure 5 (a) Average Delay Time Vs Arrival Rate for different Priority Schemes



Fig. 6 Delay Time Ratio Vs Probability



Figure 5 (b) Average Delay Time Vs Arrival Rate for different Priority Schemes



Figure 7 (a) Cell Loss Rate For Loss Sensitive Class Vs Buffer Size



Fig. 7 (b) Cell Loss Rate For Delay Sensitive Class Vs Buffer Size