Abstract

In chip manufacturing technology, the threshold of major evaluation, which shrinks chip in size
Implementation of LFSR Counter using CMOS Chip Technology

and performance, is implemented in layout level which develops the low power consumption chip, using recent CMOS, Microwind layout tools. This paper compares 3 architectures in terms of the hardware implementation, power consumption and CMOS layout using Microwind CMOS layout tool. Thus it provides solution to a low power architecture implementation of Counter in CMOS VLSI. The Microwind program allows the designer to design and simulate an integrated circuit at physical description level. Such technology is highly applicable in the design approaches for the rural development

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Index Terms

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**Keywords**
- Rural Development Technology
- Microwind Chip Technology
- Layout Level
- Lfsr
- Pass Transistor