Abstract
Two novel architecture of a CCII one is based on combination of OTA and Flipped Voltage Follower and second is based wide swing cascode current mirror are proposed in this paper. The First proposed architecture are operable at +/-1v and second is operable at 1.5V and featuring simplicity, compactness, low power consumption and bandwidths up to 100 MHz for a 1µm CMOS technology. The architecture is simulated using spice to get estimation of various characteristics parameters like X,Y and Z terminal impedances, output offset voltage, Bandwidth, Vx/Vy and Iz/Ix ratios using 1µm Technology.

Reference

- D. F. Bowers. “Applying current feedback to voltage amplifiers”. In Analogue IC design: The current mode approach. Peter Peregrinus 1990.
- G. Palumbo and S. Pennisi. Current feedback amplifiers versus voltage operational

Index Terms

Computer Science Wireless

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CFOA