Abstract

Image processing is a recent trend that is grabbing attention in almost all areas like space, medical, defence, authentication systems etc. Discrete Fourier Transforms is one of the most used transforms in image processing. Discrete Fourier Transform helps to transform the signal from spatial domain to frequency domain which is often used for filtering, correlation analysis and spectrum analysis. For DFT, computational complexity is more. Among different approaches to compute DFT, Fast Fourier Transform (FFT) is the feasible method that reduces the computational complexity. FFT can be implemented using DSP or FPGA. This paper lays a path to implement image FFT on FPGA using Intellectual Property (IP) core.

References

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