Abstract

In the modern communication system, the variety of technology leads to the developmental concepts and for that wireless technology also plays a prominent role in the field of modern communication. WiMAX has got its own limitation. The key difficulty is Deinterleaver design and it needs ample hardware if all the modern schemes and code rates have to be designed on field programmable gate array (FPGA). Floor function is one such design with a great degree impotence of hardware. This paper is an attempt to have a variable newer technology and to regulate the impotence of the hardware. Mathematical model has been done in order to remove hardware impotence and by introducing embedded multiplier in FPGA interconnection delay has been reduced. The internal multiplier of FPGA along with 16QAM and 64 QAM modulations have made this connectivity novel and efficient.

References

A Pursuit of Dynamic Address Alternator for WiMAX Deinterleaver


Index Terms

Computer Science

Wireless

Keywords

WiMAX (worldwide Interoperability For Microwave Access)  Deinterleaving.