Abstract

In this study, having a study of implementation of I2C protocol on FPGA. This study of project also contains learning anew platforms, tools like I2C bus configuration Xilinx 14. 1, Verilog. I2C protocol has easy communication without any data loss. It also has high speed compared to other protocols. I2C uses only two wire for communication. It also increases data transfer rate. I2C is used in data observation for accuracy and efficiency. The design method will be developed in Verilog, simulated on Xilinx 14. 1 and will be implemented on FPGA board.
Index Terms

Computer Science  Communication

Keywords

I2c24c02  Fpga  I2c  Master  Serial Data Communication  Slave  Spartan 3  Xilinx