Abstract

As demand of handheld devices like multimedia devices, cellular phones, etc. are increasing and we are approaching towards portable devices which are small in size and which requires large battery life. But power dissipation has become most important design factor for VLSI circuits and system in low power devices. So the Level shifter plays very critical role in low power devices. Level shifter is an interfacing circuit which can interface low core voltage to high input-output voltage. The level shifter is used to allow communication between different modules without adding any extra supply pin. This level shifter circuits are uses self biased cascode current mirror and CMOS logic gate. A new family of low power dynamic logic called
Data Driven logic is used. The simulation and measurement results were verified using a 22-nm technology.

References


Index Terms

Computer Science

Circuits And System

Keywords
Current Mirror  Level Shifter  Subthreshold Circuit.