Abstract

This paper describes the detailed study and analysis of Golumb codes used for the test vector compression in VLSI testing. The Golumb Codes are widely used for lossless Data compression due to its lower complexity in encoding & Decoding methods. Furthermore, a comparative study of various compression techniques is also presented in this paper. This paper also gives out an idea about the design of Golumb Encoder & Decoder using VHDL for the test vectors thus achieving a good height of compression ratio. In order to prove its validity, the developed algorithm is simulated using the Xilinx 9.2i and MATLAB software.
A Modified GOLUMB Encoder and Decoder for Test Vector Compression

References

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Index Terms

Computer Science  
Network Application
**Keywords**

Golumb  Encoder  Decoder And Compression