Abstract

In this paper a 4-bit algorithmic current mode Analog-to-Digital Converter (ADC) has been implemented. A vital component of this ADC is a current comparator. We have simulated three popular structures of current comparators that can be used to implement this ADC and compared their performance. The circuit has been implemented using 0.18 ?m CMOS technology with a supply voltage of 1.8V.
Performance Comparison of an Algorithmic Current-Mode ADC Implemented using Different Current Comparators

References


Index Terms

Computer Science

Signal Processing
Keywords
Algorithmic  Adc  Current-mode  Current Comparator  Current Subtractor  Current Mirror.