

Fuzzy Rule Based Load Frequency Control in a Parallel AC – DC Interconnected Power Systems through HVDC Link

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ABSTRACT:

In this paper, a fuzzy logic controller is proposed for an application of HVDC link to stabilize the frequency oscillation in a parallel AC – DC interconnected power systems. When an interconnected AC power system is subjected to a load disturbance, system frequency may be considerably disturbed and becomes oscillatory. By utilizing the system interconnections as the control channels of HVDC link, the tie line power modulation of HVDC link through interconnections is applicable for stabilizing the frequency oscillation of AC system. The conventional Integral controller does not yield adequate control performance. To overcome this problem Fuzzy Logic Controller (FLC) is employed with a set of control rules. The proposed control technique is studied for a two area non - reheat thermal power systems. From simulation results, the performance of the FLC is better during load disturbances.

KEYWORDS:

AC – DC Interconnected System, High Voltage Direct Current (HVDC) Link, Area Control Error, Load Frequency Control, Fuzzy Logic Control,

1. INTRODUCTION

Power engineers have the responsibility to deliver economical, adequate and quality power to consumers. In order to achieve this, the power system must be maintained at the desired operating level by implementing modern control strategies. The control of power systems is becoming increasingly more complex due to large interconnections. In an interconnected network, a disturbance in one line, leads to effects on the neighbouring systems change in tie-line power and frequency, causing serious problem of Load Frequency Control (FLC). LFC is a simple mechanism to maintain or restore the frequency and tie – line power flow among the interconnected power systems within the specified limit [1, 2].

LFC is a very important factor in power system operation and control for supplying sufficient and reliable electric power with good quality. There has been continuing interest in designing load frequency controller with better performance during the past 30 years. Many control strategy for LFC have been proposed since the 1970s [3]. Recently, applications of power electronics devices in AC power systems provide attractive benefits of economics and innovative technologies. In particular, High-Voltage Direct Current transmission link (HVDC link) offers major advantages in meeting these

requirements, including long distance overhead bulk power transmission, transmission between unsynchronized AC systems [4-6], and marine cable transmission.

A sophisticated advantage of HVDC link is the enhanced damping of AC transmission using power modulation via an HVDC link in a parallel AC-DC interconnected power system [4]. When an AC power system is subjected to load disturbance, the system frequency may be considerably perturbed from the operating frequency. This may cause severe problems in system frequency oscillations. The deviation of frequency oscillations, that exceed the normal limit, directly interrupts the operation of power system. Moreover, the frequency oscillations may experience serious stability problems usually in the form of low frequency oscillations due to insufficient system damping. To overcome this problem, this paper not only takes the advantage of power modulation control offered by HVDC link to enhance the system damping, but also extends to stabilize frequency oscillations in an AC power system. By utilizing the interconnections between AC power systems as control channels of power modulation of HVDC link, this creates a new application of the HVDC link to stabilize frequency oscillations.

Due to unnecessary error in the past, conventional PI controller does not provide adequate control performance. The difficulty in obtaining the optimum settling time of previously said controller is mitigated by using FLC [7-9], which gives the opportunity to describe the control action in qualitative term and symbolic form. The proposed control can also be coordinated with conventional integral control for greater efficiency.

PROBLEM FORMULATION OF PROPOSED CONTROL

A typical two area interconnected system through AC –DC links is shown in Fig.1. At area -1 there is a DC transmission line and an inverter, whereas in area -2, the HVDC link mainly consists of a rectifier. Originally area 2 supplied power PAC through AC line only, to area 1. It is assumed that in area 1 there are large loads changing suddenly, like magnetic levitation transportation, huge steel mills, are furnaces etc. These cause in area 1, sudden demand for electric power and serious problem of frequency oscillations. In area 1, in addition, there are many independent power producers, having no frequency control abilities sufficiently. As the capabilities of frequency control governors in area 1 are not sufficient, they are not capable of stabilizing the frequencies oscillations. Whereas area 2 has sufficient frequency

control capability to compensate for area-1. AC line alone cannot supply the required bulk power. To supply more power to area-1, an AC tie-line in parallel with an HVDC link is also provided from area 2. Through HVDC link, area-2 offers frequency stability to area-1[4]. The proposed method has large capability of frequency stabilisation to other interconnected areas having less capability. The proposed control can serve as a new auxiliary service for stabilising future

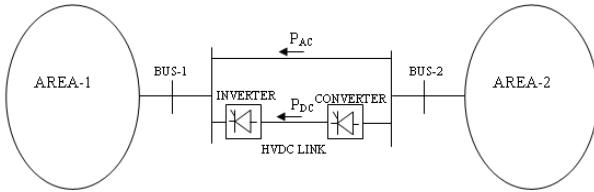


Fig. 1. An HVDC Link in a parallel AC-DC interconnected power system

regulated power systems.

DESIGN OF POWER MODULATION CONTROLLER BY HVDC LINK

Coordinated Control of HVDC Link and Governors

To simplify the control design of the power modulation controller, the concept of coordinated control of HVDC link and governors will be explained. The HVDC link is superior to the governor which is a conventional frequency control system in terms of high-speed performance. Based on this different speed performance, a coordinated control of HVDC link and governors is as follows. When some sudden load disturbances occur in an area, an HVDC link quickly starts the control system to suppress the peak value of transient frequency deviation. Subsequently, governors eliminate the steady state error of the frequency deviation. Another advantage in considering the different speed performance is that the dynamics of governors in both areas can be neglected in the control design of HVDC link for simplicity.

Control Design

The Linearized Model of Interconnected Power System without Governors for Control Design of Power Modulation Controller of HVDC Link is delineated in Fig. 2 where the dynamics of governors in both areas are eliminated. The power modulation controller is modeled as a proportional controller of active power. It should be noted that the power modulation output of HVDC link (ΔP_{DC}), acting positively on an area, reacts negatively on another area in an interconnected system. ΔP_{DC} , therefore, flows into both areas with different sign (+, -), simultaneously. Here, to simplify the control design, the state equation of the system in Fig 2 where the time constant TDC is ignored, can be expressed as

$$S = \begin{bmatrix} \cdot \\ \Delta F_1 \\ \cdot \\ \Delta P_{AC} \\ \cdot \\ \Delta F_2 \end{bmatrix} = \begin{bmatrix} 0 & -\frac{1}{T_{PS_1}} & -\frac{K_{PS_1}}{T_{PS_1}} & 0 \\ -\frac{1}{T_{PS_1}} & 0 & -\frac{K_{PS_1}}{T_{PS_1}} & 0 \\ 2\pi T_{12} & 0 & 0 & -2\pi T_{12} \\ 0 & -2\pi T_{12} & 0 & 0 \\ 0 & 0 & -\frac{a_{12}K_{PS_2}}{T_{PS_2}} & -\frac{1}{T_{PS_2}} \\ 0 & 0 & -\frac{1}{T_{PS_2}} & -\frac{a_{12}K_{PS_2}}{T_{PS_2}} \end{bmatrix} \begin{bmatrix} \Delta F_1 \\ \Delta P_{AC} \\ \Delta F_2 \end{bmatrix} + \begin{bmatrix} -\frac{K_{PS_1}}{T_{PS_1}} \\ 0 \\ -\frac{a_{12}K_{PS_2}}{T_{PS_2}} \end{bmatrix} \begin{bmatrix} \Delta P_{DC} \end{bmatrix} \quad \text{---(1)}$$

Equation (1) is referred to as system "S". The variables and parameters of in Fig 2 are defined as follows. Δf_1 , Δf_2 are frequency deviations of areas 1 and 2 respectively. ΔP_{AC} is an AC tie line power deviation between areas 1 and 2. ΔP_{DC} is a power modulation by HVDC link. ΔP_{12} is the total tie line power deviations ($\Delta P_{AC} + \Delta P_{DC}$). Let M_1 , M_2 be the inertia constants of areas 1 and 2. D_1 , D_2 be the damping coefficients of areas 1 and 2. A_{12} be an area capacity ratio between areas 1 and 2.

Here, the control scheme for power modulation of HVDC link (ΔP_{DC}) is designed by the eigenvalue assignment method, so that the dynamic aspect of the inter-area oscillation mode between areas 1 and 2 is specified. This mode can be explicitly expressed after applying the variable transformation.

$$Y = CX \quad \text{---(2)}$$

Where, C is a transformation matrix, Y is the transformed state vector, and X is the state vector in (1). Therefore, the transformed system can be expressed as

$$\begin{bmatrix} \cdot \\ \Delta Y_1 \\ \cdot \\ \Delta Y_2 \\ \cdot \\ \Delta Y_3 \end{bmatrix} = \begin{bmatrix} \alpha & \beta & 0 \\ -\beta & \alpha & 0 \\ 0 & 0 & \lambda \end{bmatrix} \begin{bmatrix} \Delta Y_1 \\ \Delta Y_2 \\ \Delta Y_3 \end{bmatrix} + \begin{bmatrix} \gamma_1 \\ \gamma_2 \\ 0 \end{bmatrix} \begin{bmatrix} \Delta P_{DC} \end{bmatrix} \quad \text{---(3)}$$

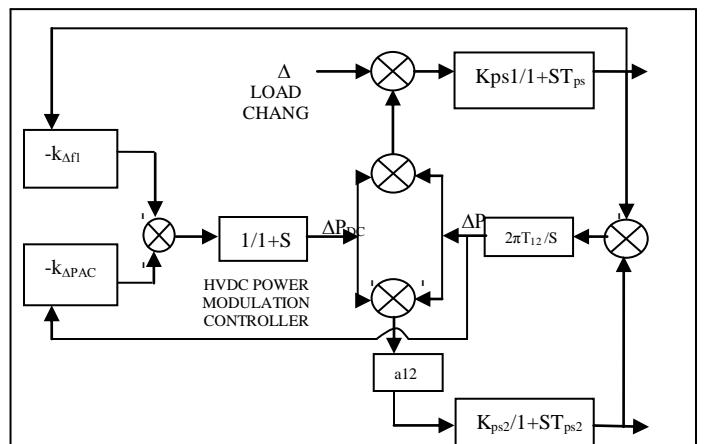


Fig. 2. Linearized Model of Interconnected Power System without Governors for Control Design of

The transformed coefficient matrix of equation (3) consists of two diagonal blocks with complex eigenvalue $\alpha \pm j\beta$ and real eigenvalue λ . The complex eigenvalue physically correspond to the inter-area oscillation mode, while the real eigenvalue represents the system inertia center mode. From the physical view point, it should be noticed that the HVDC link between two areas is effective to stabilize the inter area mode only, and therefore the input term of (3) corresponding to Δy_3 is zero. This means that the HVDC link cannot control the inertia center mode. To solve this crux, it is expected that the governors in both areas are responsible for suppressing the frequency deviation due to the inertia mode. Therefore, the power modulation controller of HVDC link is designed based on stabilizing the inter-area mode.

In order to extract the subsystem where the inter area oscillation mode between areas 1 and 2 is preserved, from the system S , the technique of overlapping decompositions¹⁵ is applied. First, the state variables of the original system S are classified into three groups, ie $x_1 = [\Delta f_1]$, $x_2 = [\Delta P_{AC}]$ and $x_3 = [\Delta f_2]$. According to the process of overlapping decompositions^[4], the system S can be expanded as

$$\tilde{S} = \begin{bmatrix} \bullet \\ Z_1 \\ \bullet \\ Z_2 \end{bmatrix} = \begin{bmatrix} A_{11} & A_{12} & 0 & A_{13} \\ A_{21} & A_{22} & 0 & A_{23} \\ A_{21} & 0 & A_{22} & A_{23} \\ A_{31} & 0 & A_{32} & A_{33} \end{bmatrix} \begin{bmatrix} Z_1 \\ Z_2 \end{bmatrix} + \begin{bmatrix} B_{11} \\ B_{21} \\ B_{21} \\ B_{31} \end{bmatrix} [\Delta P_{DC}] \quad \dots \dots (4)$$

Where

$$Z_1 = [x_1^T, x_1^T]^T \text{ and } Z_2 = [x_1^T, x_1^T]^T. \text{ The}$$

element a_{ij} , b_{i1} ($i, j = 1, 2, 3$) correspond to each element in the coefficient matrix in (1). The system S in (4) can be decomposed into two interconnected overlapping subsystems,

$$\tilde{S}_1 = \dot{Z}_1 = \left(\begin{bmatrix} A_{11} & A_{12} \\ A_{21} & A_{22} \end{bmatrix} [Z_1] + \begin{bmatrix} B_{11} \\ B_{21} \end{bmatrix} [\Delta P_{DC}] \right) + \begin{bmatrix} 0 & A_{13} \\ 0 & A_{23} \end{bmatrix} [Z_2] \quad \dots \dots (5)$$

$$\tilde{S}_2 = \dot{Z}_2 = \left(\begin{bmatrix} A_{22} & A_{23} \\ A_{32} & A_{33} \end{bmatrix} [Z_2] + \begin{bmatrix} B_{21} \\ B_{31} \end{bmatrix} [\Delta P_{DC}] \right) + \begin{bmatrix} A_{21} & 0 \\ A_{31} & 0 \end{bmatrix} [Z_1] \quad \dots \dots (6)$$

The state variable x_2 , ie the AC tie line power deviation (ΔP_{DC}) between both areas, is repeatedly included in both subsystems, which implies “Overlapping Decompositions”.

For system stabilization, consider two interconnected subsystems S_1 and S_2 . The terms in the right hand sides of (5) and (6) can be separated into the decoupled subsystems (as indicated in the parenthesis in (5) and (6)) and the interconnected subsystems. As mentioned in Ikeda et al¹⁵, if each decoupled subsystem can be stabilized by its own input, the asymptotic stability of the interconnected overlapping subsystems S_1 and S_2 are maintained. Moreover, the asymptotic stability of the original system S is also guaranteed.

Consequently, the interactions with the interconnected subsystems in (5) and (6) are regarded as perturbations and are neglected during control design. As a result, the decoupled subsystems of S_1 and S_2 can be expressed as

$$\tilde{S}_{D1} = Z_1 = \begin{bmatrix} a_{11} & a_{12} \\ a_{21} & a_{22} \end{bmatrix} Z_1 + \begin{bmatrix} b_{11} \\ b_{21} \end{bmatrix} \Delta P_{DC} \quad \dots \dots (7)$$

$$\tilde{S}_{D2} = Z_2 = \begin{bmatrix} a_{22} & a_{23} \\ a_{32} & a_{33} \end{bmatrix} Z_2 \quad \dots \dots (8)$$

In (7) and (8), there is a control input ΔP_{DC} appearing only in subsystem S_{D1} . Here, the decoupled subsystem S_{D1} is regarded as the designed system, which can be expressed as

$$\begin{bmatrix} \dot{A}_{F1} \\ \Delta P_{AC} \end{bmatrix} = \begin{bmatrix} -\frac{1}{T_{PS_1}} & -\frac{K_{PS_1}}{T_{PS_1}} \\ \frac{K_{PS_1}}{2\pi T_{12}} & 0 \end{bmatrix} \begin{bmatrix} \Delta F_1 \\ \Delta P_{AC} \end{bmatrix} + \begin{bmatrix} -\frac{K_{PS_1}}{T_{PS_1}} \\ 0 \end{bmatrix} [\Delta P_{DC}] \quad \dots \dots (9)$$

It can be verified that the eigenvalues of (9) are $\alpha \pm j\beta$, ie, the inter-area oscillation mode in the system S . It should be noticed that by virtue of overlapping decompositions, the physical characteristic of the original system is still preserved after the process. Here, the control purpose of HVDC link is to damp the peak value of frequency deviation after sudden load disturbance. By eigenvalue assignment method, the feedback control scheme of ΔP_{DC} can be expressed as

$$\Delta P_{DC} = -k_{\Delta f_1} \Delta f_1 - k_{\Delta P_{AC}} \Delta P_{AC} \quad \dots \dots (10)$$

Note that, the state feedback scheme is constructed by two measurable signals, ie a frequency deviation of area 1 and an AC tie-line power deviation. The integral controller has received a great deal of attention in the process control areas. Integral controller is a device that produces an output signal, which is integral to the input signal. The system control outputs, which depends on the area control error, that is given by

$$U_1 = K_{I_1} \int ACE_1(t) dt \quad \dots \dots (11)$$

$$U_2 = K_{I_2} \int ACE_2(t) dt \quad \dots \dots (12)$$

$$\text{Where } ACE_1 = \beta_1 \Delta F_1 + \Delta P_{+ie1}$$

$$ACE_2 = \beta_2 \Delta F_2 + \Delta P_{+ie2}$$

$$\Delta P_{+ie2} = a_{12} \Delta P_{+ie1}$$

β is the biasing factor and U_1 & U_2 is the system control output. The integral controller is successful approach towards the zero steady state error in the frequency of the system. The conventional integral controller, the gain KI has been determined using Integral Square Error (ISE) criterion. The objective function used for this technique is

$$J = \int_0^t (\Delta F_1^2 + \Delta F_2^2 + \Delta P_{tie}^2) dt \quad \text{----- (13)}$$

Because of the complexity and multivariable condition of the power system, a conventional model may not give satisfactory solution. This inference is further reinforced by fuzzy logic controller which is useful in mitigating a wide range of control problem.

Fuzzy Logic Controller:

Based on the earlier research results of this stabilisation of frequency in the interconnected power system [10-12], we can design the fuzzy logic controller. Fuzzy set theory and fuzzy logic establish the rule of a nonlinear mapping. The use of fuzzy sets provides a basic for a systematic ways for the application of uncertain and indefinite models. A two area interconnected power system with parallel AC – DC link is considered here as shown in fig.3.

The overall system can be modelled as a multi-variable system in the form of

$$\bullet \quad \mathbf{X} = \mathbf{Ax} + \mathbf{Bu} + \mathbf{Ed} \quad \text{----- (14)}$$

Where the System State vector \mathbf{x} consists of nine variables as

$$\begin{aligned} \text{System control input is } [\mathbf{u}] &= \begin{vmatrix} u_1 \\ u_2 \end{vmatrix} = \begin{vmatrix} \Delta P_{c1} \\ \Delta P_{c2} \end{vmatrix} \\ \text{System disturbance input vector is } [\mathbf{d}] &= \begin{vmatrix} d_1 \\ d_2 \end{vmatrix} = \begin{vmatrix} \Delta P_d \\ \Delta P_{d2} \end{vmatrix} \end{aligned}$$

A, B and E are system state matrix, distribution matrix and disturbance distribution matrix of appropriate dimensions.

The main objective is to minimize the mean square error (MSE) of the area control error (ACE) due to step type load disturbance [13-15]. The objective function of the load frequency controller [4] is given by,

$$\text{ACE}_i = \beta \Delta F_i + \Delta P_{tiei} \quad \text{----- (15)}$$

Where i - vary from 1 to 2

β is the biasing factor.

The design of LFC can be normally divided into three areas namely allocation of area inputs, determination of rules and defuzzifying of output into a real value.

The method of fuzzification has found increasing application in power system. In this FLC, membership function (MF) specifies the degree to which a given input belongs to a set. In the case of FLC, seven membership functions in triangular shape have been chosen for the inputs of ΔF , ΔP_{tiei} and output (u). The linguistic descriptions of input membership functions are Negative Large (NL), Negative Medium (NM), Negative Small (NS), Zero (ZE), Positive Small (PS), Positive Medium (PM) and Positive Large (PL). The output membership functions are Negative High (NH), Negative Medium (NM), Negative Small (NS), Zero (ZE), Positive Small (PS), Positive Medium (PM) and Positive High (PH). The fuzzy membership functions for inputs and output are shown in Figs. 4 and 5. The union maximum operation has been selected for the fuzzy implication. For the two- input fuzzy system, it is generally expressed as

$$\mu_{Ai(x1) \cup Ai(x2)} = \max \{ \mu_{Ai(x1)}, \mu_{Ai(x2)} \} \quad \text{----- (16)}$$

where $A_{i(x1)}$ and $A_{i(x2)}$ are input fuzzy sets. The rule base of the fuzzy controller relates the premise (ΔF and ΔP_{tiei}) to consequent (u). The structure of the control rules of the fuzzy controller with two inputs and an one output is expressed as

$$\text{If } (\Delta F \text{ is NM or } \Delta P_{tiei} \text{ is NS}) \text{ then control signal } u \text{ is NH} \quad \text{----- (17)}$$

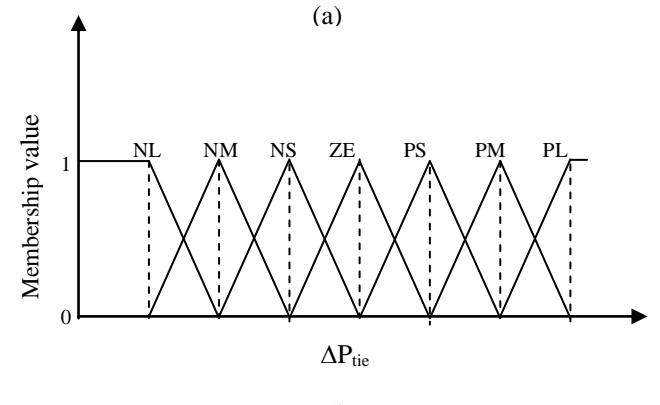
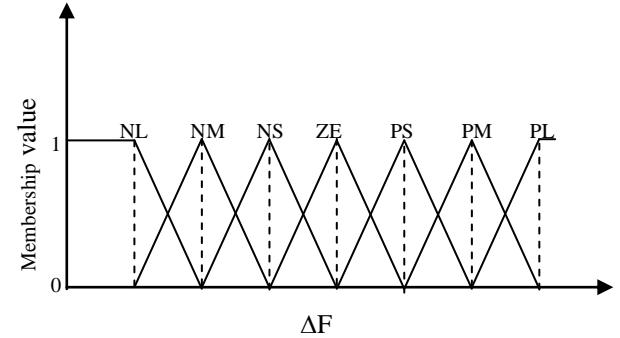


Fig.5. Input membership functions of fuzzy controller (a) ΔF ; (b) ΔP_{tie}

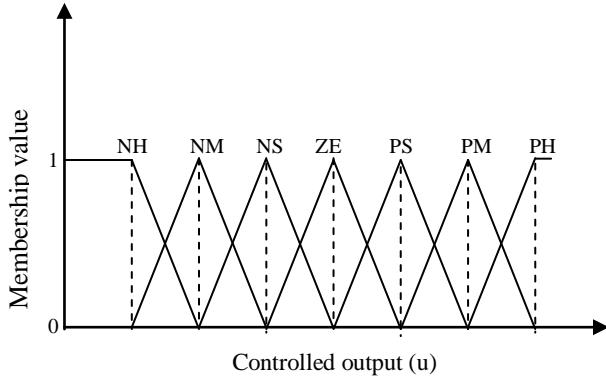


Fig.6 Output membership functions of fuzzy controller

Table 1 lists 49 linguistic fuzzy rules for the fuzzy controller. The centroid difuzzification has been made to find the crisp value of output. The centroid difuzzification is defined as

$$U^{\text{Crisp}} = \frac{\sum_{i=1}^n u_i \mu(u_i)}{\sum_{i=1}^n \mu(u_i)} \quad \text{----- (18)}$$

Table – 1, Fuzzy Control rules

u	ΔP_{tie}							
		NL	NM	NS	ZE	PS	PM	PL
ΔF	NL	NH	NH	NH	NH	NS	N M	ZE
	NM	NH	NH	NH	NH	N M	ZE	PM
	NS	NH	NH	NS	N M	ZE	PM	PS
	ZE	NH	NS	NM	ZE	PM	PS	PH
	PS	NS	NM	ZE	PM	PS	PH	PH
	PM	N M	ZE	PM	PS	PL	PH	PH
	PL	ZE	PM	PS	PH	PH	PH	PH

Where U^{Crisp} is the output of the fuzzy controller, u_i denotes the centre of the membership function of the consequent of the i -th rule, μ denotes the membership value for the rule's premise and n represents the total number of fuzzy rules.

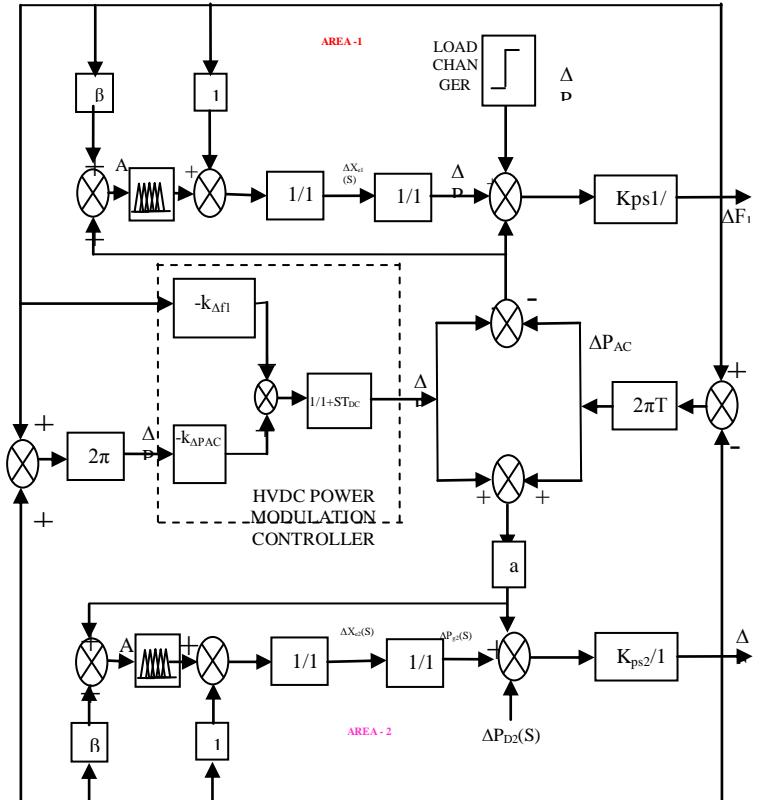


Fig. 3. Modeling of HVDC power modulator in the two area thermal non reheat with parallel AC-DC power system

Simulation Result and Discussion:

In this paper, a two area non reheat interconnected system is used to design and evaluate the effects of the power modulation controller of HVDC link. The system data are given in the appendix. The simulation study is carried out by the software "Mat lab".

The system is simulated for a step- load such as a large steel roll mill and arc- furnace factory, an increase 10% (0.1p.u.M.W.) occurs in area- 1. Due to this, change in dynamic responses of the system has been observed. Fig.

4 and 5 indicates the frequency deviation of area 1&2 and fig. 6 indicates the tie line power deviation for a step load disturbance of area- 1.

Similarly the system is simulated for a step- load disturbance of 30% (0.3 p.u.M.W.) occurs in area- 1. For this disturbance, the frequency deviation of area 1&2 is shown in fig. 7&8. The tie line power deviation for a step load disturbance of area- 1 is shown in Fig.9. The effectiveness of the proposed control method is demonstrated by comparing the integral of the square of the error (ISE), integral of the absolute value of the error (IAE) and settling time.

The ISE and IAE are given as

$$ISE = \int_0^{\infty} ACE^2 dt \quad \dots \dots \dots (19)$$

$$IAE = \int_0^{\infty} |ACE| dt \quad \dots \dots \dots (20)$$

The performance comparison for the step load disturbance of 10% and 30% are presented in Table- 2

Table-2, Performance Comparison of different Control Algorithms

Types of Control	ISE		IAE		Settling Time T_s (S)	
	10% Disturbance	30% Disturbance	10% Disturbance	30% Disturbance	10% Disturbance	30% Disturbance
Integral Control	0.0156	0.141	0.24	0.75	16.2	18.6
Fuzzy Control	0.00138	0.011	0.056	0.168	9.4	11.4

It is observed that the fuzzy logic controller exhibits relatively good performances and fast settling time. The conventional integral controller does not yield adequate control performance. Simulation result concludes that fuzzy logic controller yields much improved control performance than the conventional integral controller.

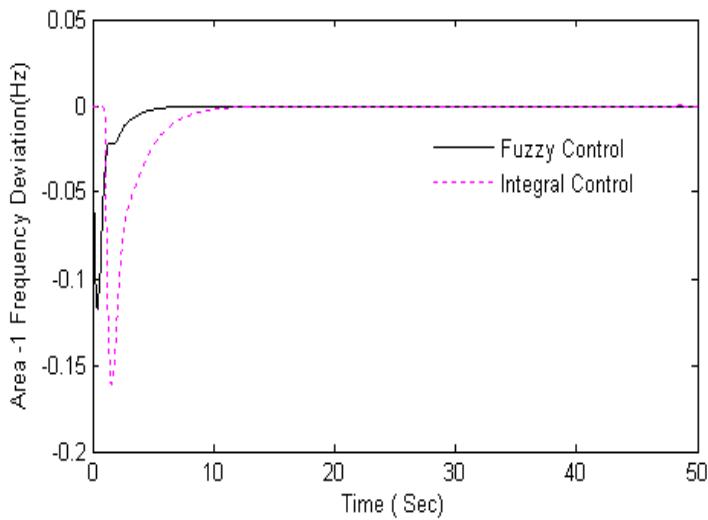


Fig.4. Frequency deviation in area-1(10% Disturbance)

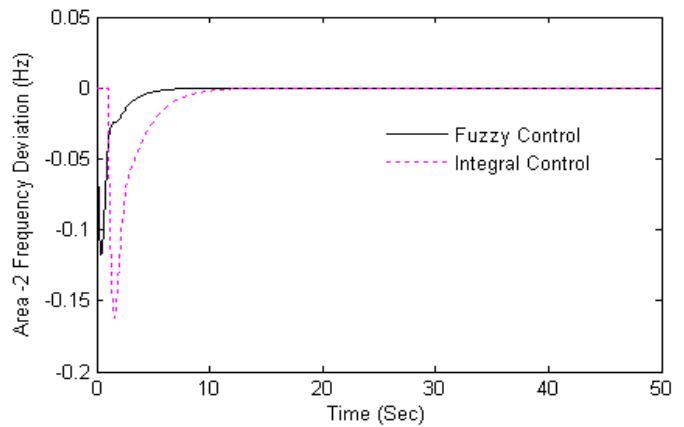


Fig.5. Frequency deviation in area-2(10% Disturbance)

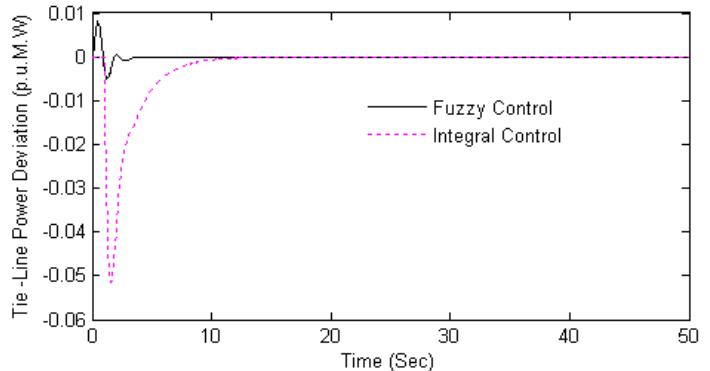


Fig.6. Tie line power deviation (10% Disturbance)

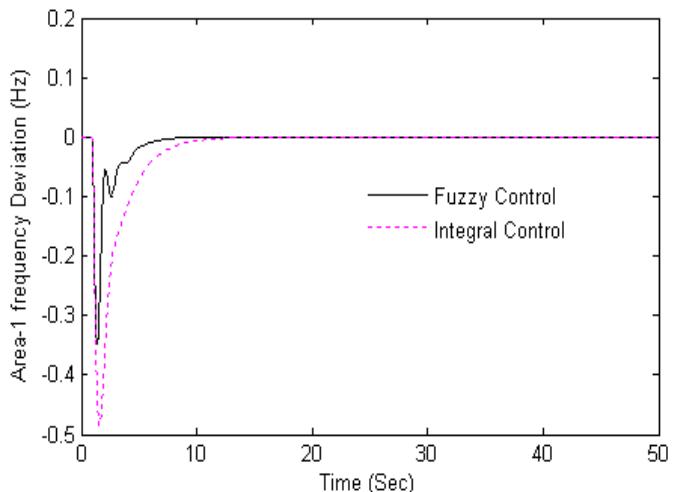


Fig.7. Frequency deviation in area-1(30% Disturbance)

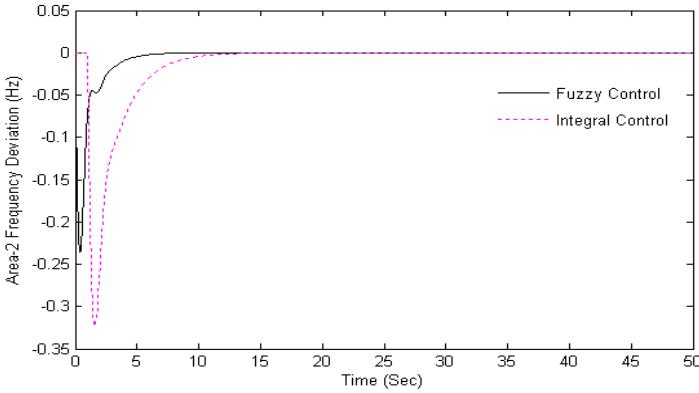


Fig.8. Frequency deviation in area-2(30% Disturbance)

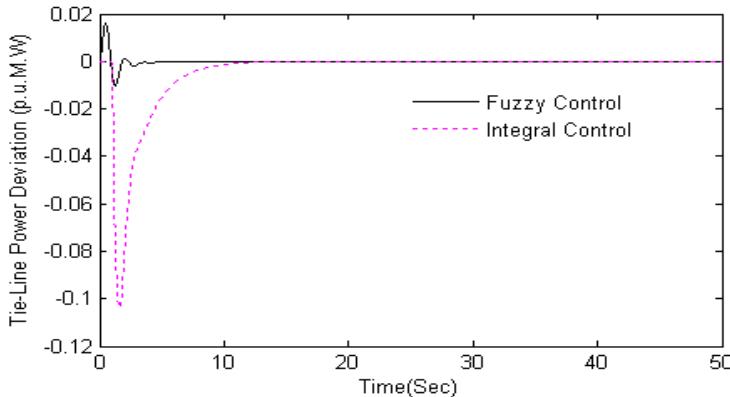


Fig.9. Tie line power deviation (30% Disturbance)

Conclusion:

In this paper, a sophisticated method for stabilizing frequency oscillations in a parallel AC- DC interconnected power system through an HVDC link to stabilize frequency oscillations in AC power system can be achieved. The HVDC power modulation controller is developed by applying decomposition and eigenvalue assignment techniques. By simulation study, the fuzzy logic controller is very effective in suppressing the frequency oscillations caused by rapid load disturbances. For further study, the proposed control design of HVDC link will be extended to stabilize the frequency oscillations in a multi area interconnected power system.

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Appendix:

Two area Non - reheat thermal power system

$$\beta_1 = \beta_2 = 0.425 \text{ p.u .M..W/Hz.}$$

$$T_{t1} = T_{t2} = 0.3 \text{ sec}$$

$$T_{g1} = T_{g2} = 0.08 \text{ sec}$$

$$K_{ps1} = K_{ps2} = 120 \text{Hz/p.u. MW}$$

$$2\pi T_{12} = 0.545$$

$$T_{ps1} = T_{ps2} = 20 \text{ sec}$$

$$R_1 = R_2 = 2.4 \text{ Hz/p.u MW.}$$

$$K_{\Delta f1} = -0.3201$$

$$K_{\Delta PAC} = -2.144$$