Abstract

In this paper, the aim is to design an embedded system which will efficiently assist in capturing motorists who break signals/overspeed at a monitored traffic junction. Signal breaking is a big menace in India currently, and such a system is not in place in any Indian city. Hence this system of documenting traffic violations has much potential. The design of a prototypical system using components like a Passive Infrared (PIR) sensor, a digital camera module, and a Programmable System on Chip 4 (PSoC 4) platform is outlined in the paper. The system designed will enable the penalization of those who violate traffic laws.
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Index Terms

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Embedded Systems

Keywords

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