Abstract

With the advancement of IT, various high as well as low powered electronics and electrical devices have penetrated in the commercial and domestic markets. Usages of such types of electronics system are found to give rise occasionally to harmonics owing to non-linear loads. From more than a decade, there are sets of effective research contribution towards introducing a technique for suppression such harmonics using filters. However, till date there is no evidence of any standard model or framework or any landmark ideas that has solved the issue of harmonic suppression in totality. Thus, the intention of this manuscript is to discuss harmonics with respect to active power filters where the prominent focus is laid down to understand the effectiveness of priorly introduced techniques. The study presents a state-of-art review of techniques of harmonic suppression with an aid of the research gap.

References

An Insight to Harmonic Suppression Techniques with Power Filters in Power Electronics

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