Abstract

A adder is one of the key hardware blocks in most digital and high performance systems such as FIR filters, digital signal processors and microprocessors etc. With advances in technology, many researchers have tried and are trying to design many types of adder such as ripple carry adder, carry skip adder, carry a look head adder and carry select adder. Among this adder carry select adder is the high speed, low power consumption and hence less area or even combination of them in adder. However area and speed are two conflicting constraints.

References


Technology Trends in Electronics, Communication and Networking
978-1-4673-1627-9/12/$31.00 ©2012 IEEE.
8. Yotmgjoon Kim and Lee-Sup Kim, “A Low Power Carry Select Adder with Reduced Area”, 0-7803-6685-9/01/$10.0002001 IEEE.

Index Terms

Computer Science Circuits and Systems

Keywords

Ripple Carry Adder, Carry Select Adder (CSLA), Booth Encoder (BEC)