In this paper, an improved approach for the modeling of power thyristors is presented. A modified two-transistor configuration based on the Gummel-Poon model is applied. This model takes into account the conductivity modulation and carrier-carrier scattering by using nonlinear current sources. The current gain of the transistor is studied relating it to the injection level in order to provide a more insight of some SPICE parameters. The design parameters of the thyristor are related to circuit parameters using some analytical expressions. Then, the SPICE model parameters are extracted using Silvaco. The simulation results are compared with measurements showing good agreement indicating that the developed model could efficiently describe the performance of the thyristor under various practical operating conditions.
Thyristor Compact Modeling based on Gummel-Poon Model Including Parameter Extraction Procedure

References

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Index Terms

Computer Science

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Keywords

Modeling  Gummel-Poon  Conductivity Modulation  Design Parameters

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