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Abstract

In this paper fast locking CMOS phase locked loop is proposed. It is designed using Cadence

virtuoso gpdk 45nm CMOS technology. It is used 1 volt power supply for operation of the circuit. This proposed circuit will be very useful in clock generation in microprocessor, frequency synthesizer for cell phone, fast locking in digital aid circuits.

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Index Terms

Computer Science

Circuits And Systems

Keywords

Phase Locked Loop (pll) Phase Detector (pd) Charge Pump Voltage Controlled Oscillator (vco) Loop Filter

Frequency Divider.