Abstract

Nowadays, reversible computing is more fascinative research area to curtail power dissipation in comparison of conventional computing. In conventional computing, logic circuit dissipates more power by losing bits of information. Reversible computing recovers from losing bits of information through same number of output vector from same number of input vector and thus decreases the power dissipation. Since there are different cost considerations such as garbage outputs, gate count, quantum cost methods for specific cost reductions may be established.
A Survey on Reversible Logic Gates

References

- Majid Mohammdi "Behavioral Model of V and V+ gates to implement the reversible circuits using quantum cost", 2008, IEEE.

Index Terms

Computer Science  Circuits And Systems

Keywords
