Abstract

Increasing complexity and higher performance requirements of modern integrated circuits have naturally led to very high power consumption. Dynamic voltage scaling (DVS) is well studied and known to be successful in reducing energy consumption. Dynamic frequency scaling (DFS) is another technique to reduce energy consumption. As technology scale to nanometre, leakage power will become prominent. In this paper the effect on energy consumption of the
system while applying both DVS and DFS analysed. This study shows that by applying DFS with DVS the total energy consumption get decreased. For energy estimation process in this work high level power estimation method is used.

References

- SimpleScalar maintenance release available http://www. simplescalar. org/
Index Terms

Computer Science

Power Systems

Keywords

Dvs  Dfs  Energy