Abstract

With the fast progression of data exchange in electronic way, information security is becoming more important in data storage and transmission. Because of widely using images in industrial process, it is important to protect the confidential image data from unauthorized access. This paper presents the design of a 128 bit encoder using AES Rijndael Algorithm for image encryption. The AES algorithm defined by the National Institute of Standard and Technology (NIST) of United States has been widely accepted. Optimized and Synthesizable VHDL code is developed for the implementation of 128-bit data encryption and process. Xilinx
Simulation of Image Encryption Using AES Algorithm

ISE9.2i software is used for synthesis. Timing simulation is performed to verify the functionality of the designed circuit.

Reference

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Index Terms

Computer Science Image Processing
Key words

Advanced Encryption Standard
Rijndael
S-box